

Compal Confidential

Schematics Document

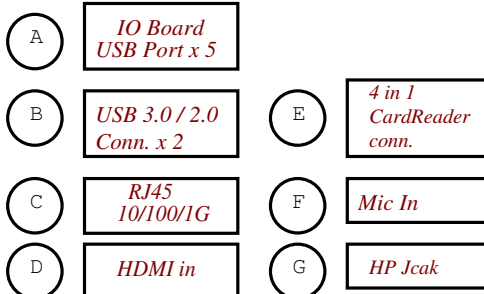
Intel Ivy Bridge Platform

www.aitech1.ru
Selene 3C

LA-8351P

| | | | | | |
|---|------------|--------------------|------------|--------------------------|---------------------------|
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| | | | | Custom | LA-8351P |
| | | | | Date: | Monday, December 26, 2011 |
| | | | | Sheet | 1 of 56 |
| | | | | Rev | 0.1 |

1600MHz 1.5V

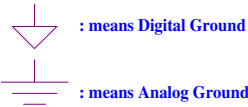


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|---|--------------------|-----------------|------------|---|--|
| Security Classification | Compal Secret Data | | | <div> <div> <i>Compal Electronics, Inc.</i> </div> <div> <i>Block Diagram</i> </div> </div> | |
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| | | | | (Sheet 2 of 56) | |

Voltage Rails (O MEANS ON X MEANS OFF)

| <div>power plane</div> <div>State</div> | +RTCVCC | B+ | +5VALW +3VALW +12VALW +12V_AMP | +3V_PCH | +1.5V +1.05V +3V | +5VS +3VS +1.5VS +VCC_GFXCORE +VCCP +CPU_CORE +1.8VS +0.75VS +VCCSA +12VS |
|---|---------|----|---|---------|--------------------------------|--|
| S0 | O | O | O | O | O | O |
| S3 | O | O | O | O | O | X |
| S4 / S5 | O | O | O | O | X | X |

Symbol Note :



@ : means just reserve , no build
CONN@ : means ME part.
RF@ : means RF model , need RF build

Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

Install below 43 level BOM structure for ver. 0.1

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SMBUS Control Table

| | SOURCE | SODIMM | EC-KB930 | | | | | |
|--------------------------|--------|--------|----------|--|--|--|--|--|
| SMB_EC_CK1 SMB_EC_DA1 | EC | X | X | | | | | |
| SMBCLK SMBDATA | PCH | V | X | | | | | |
| SML0CLK SML0DATA | PCH | X | X | | | | | |
| SML1CLK SML1DATA | PCH | X | V | | | | | |
| SMBCLK2 SMBDATA2 | PCH | X | V | | | | | |

QBA00 Board ID Table for AD channel

| | | | | |
|----------|-------------|-------------|-------------|-------------|
| Vcc | 3.3V +/- 5% | | | |
| Ra | 100K +/- 5% | | | |
| Board ID | Rb | VAD_BID min | VAD_BID typ | VAD_BID max |
| 0 | 0 | 0 V | 0 V | 0.155 V |
| 1 | 8.2K +/- 5% | 0.168 V | 0.250 V | 0.362 V |
| ★ 2 | 18K +/- 5% | 0.375 V | 0.503 V | 0.621 V |
| 3 | 33K +/- 5% | 0.634 V | 0.819 V | 0.954 V |
| 4 | 56K +/- 5% | 0.958 V | 1.185 V | 1.359 V |
| 5 | 100K +/- 5% | 1.372 V | 1.650 V | 1.838 V |
| 6 | 200K +/- 5% | 1.851 V | 2.200 V | 2.420 V |
| 7 | NC | 2.433 V | 3.300 V | 3.300 V |

QBA00 ES1
QBA00 ES2
QBA00 PP

Voltage Rails

| Power Plane | Description | S1 | S3 | S5 |
|-------------|--|----|-----|-----|
| +CPU_CORE | Core voltage for CPU | ON | OFF | OFF |
| +VGFX_CORE | Graphics voltage for CPU | ON | OFF | OFF |
| +0.75VS | 0.75V switched power rail for DDR terminator | ON | OFF | OFF |
| +1.05VS | 1.05V switched power rail for CPU | ON | OFF | OFF |
| +1.05VS_PCH | 1.05V switched power rail for PCH | ON | OFF | OFF |
| +1.5V | 1.5V power rail for DDRIII | ON | ON | OFF |
| +1.5VS | 1.5V switched power rail | ON | OFF | OFF |
| +1.8VS | 1.8V switched power rail | ON | OFF | OFF |
| +3VALW | 3.3V always on power rail once PS_ON# low | ON | ON | ON |
| +3.3V_LAN | 3.3V power rail for LAN | ON | ON | OFF |
| +3VS | 3.3V switched power rail | ON | OFF | OFF |
| +V_3.3V | 3.3V power rail once Adapter plug-in | ON | ON | OFF |
| +V_5V | 5V power rail once Adapter plug-in | ON | ON | OFF |
| +5VALW | 5V always on power rail once PS_ON# low | ON | ON | ON |
| +5VS | 5V switched power rail | ON | OFF | OFF |
| +RTCVCC | RTC power | ON | ON | ON |
| +12VALW | 12V always on power rail once 12VA_EN high | ON | ON | ON |
| +12VS | 12V switched power rail | ON | OFF | OFF |
| +12V_AMP | 12V power rail for Speaker AMP | ON | ON | ON |

| PCH USB Port List | | | | |
|-------------------|------|------|-------------|-------|
| USB 2.0 | | Port | | |
| EHCI1 | RMH0 | 0 | USB3.0 | USB0 |
| | | 1 | USB3.0 PORT | |
| | | 2 | USB3.0 | USB1 |
| | | 3 | USB3.0 PORT | |
| | | 4 | NC | |
| | | 5 | Camera | USB4 |
| | | 6 | RF | USB5 |
| EHCI2 | RMH1 | 7 | NC | |
| | | 8 | USB Conn. | USB8 |
| | | 9 | USB Conn. | USB9 |
| | | 10 | USB Conn. | USB10 |
| | | 11 | USB Conn. | USB11 |
| | | 12 | USB Conn. | USB12 |
| | | 13 | NC | |

| PCH PCI-Express List | |
|----------------------|-------------|
| PCIE1 | LAN |
| PCIE2 | Card reader |
| PCIE3 | WLAN |
| PCIE4 | TV Card |
| PCIE5 | NC |
| PCIE6 | NC |
| PCIE7 | NC |
| PCIE8 | NC |

| PCH SATA Port List | |
|--------------------|-----|
| SATA0 | ODD |
| SATA1 | HDD |
| SATA2 | NC |
| SATA3 | NC |
| SATA4 | NC |
| SATA5 | NC |

| PCH SM Bus Address | | | |
|--------------------|-------------|-----|-------------|
| Power | Device | HEX | Address |
| +3VS | DDR(JDDR11) | | 1010 000X b |
| +3VS | DDR(JDDR11) | | 1010 001X b |
| +3VS | DDR(JDDR12) | | 1010 010X b |
| +3VS | DDR(JDDR12) | | 1010 011X b |

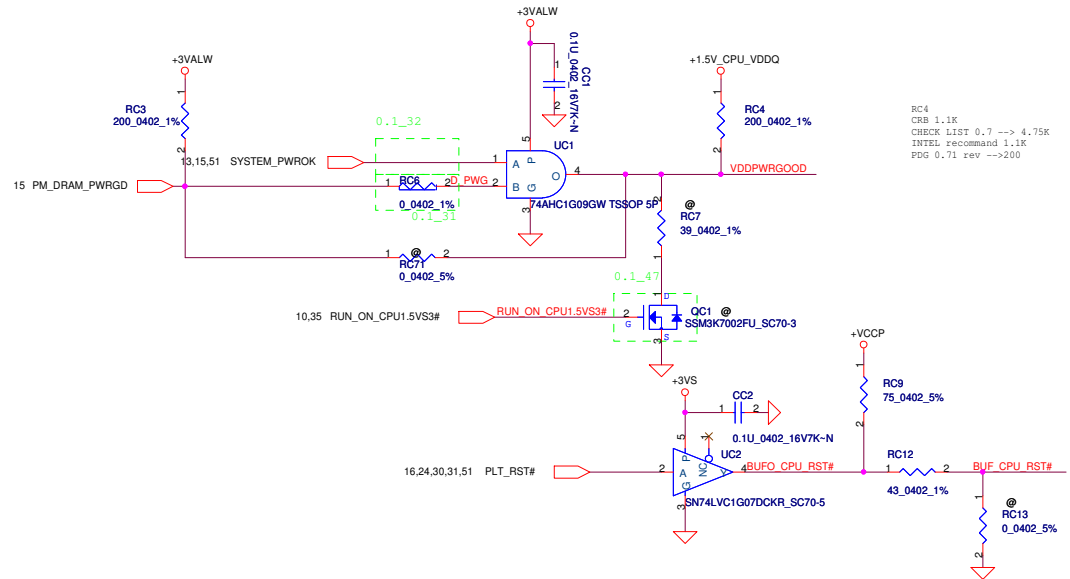
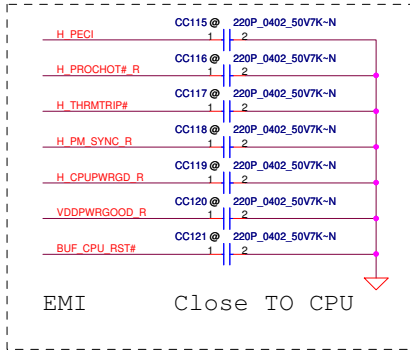
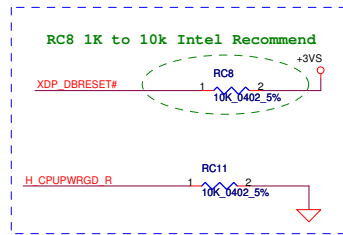
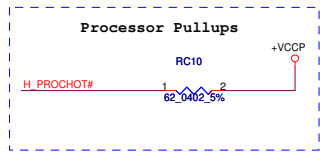
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SKU ID(Project) Table

| | | | | | Vcc | 3.3V +/- 5% | | | | |
|--------|-----|-----|------------|----|----------|-------------|-------------|-------------|-------------|--|
| SKU ID | CPU | | Panel size | | Rd | 100K +/- 5% | | | | |
| | 45w | 35w | 21.5 | 20 | Board ID | Rb | VAD_BID min | VAD_BID typ | VAD_BID max | |
| 1 | * | | * | | 0 | 0 | 0 V | 0 V | 0.155 V | |
| | * | | * | | 1 | 8.2K +/- 5% | 0.168 V | 0.250 V | 0.362 V | |
| | * | | | * | 2 | 18K +/- 5% | 0.375 V | 0.503 V | 0.621 V | |
| | * | | | * | 3 | 33K +/- 5% | 0.634 V | 0.819 V | 0.954 V | |
| 2 | | * | * | | 4 | 56K +/- 5% | 0.958 V | 1.185 V | 1.359 V | |
| | | * | * | | 5 | 100K +/- 5% | 1.372 V | 1.650 V | 1.838 V | |
| 3 | | * | | * | 6 | 200K +/- 5% | 1.851 V | 2.200 V | 2.420 V | |
| | | * | | * | 7 | NC | 2.433 V | 3.300 V | 3.300 V | |

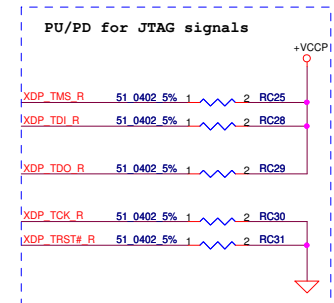
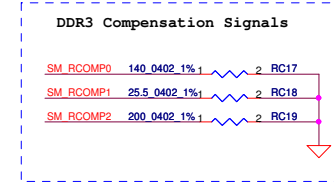
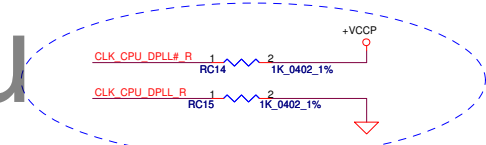
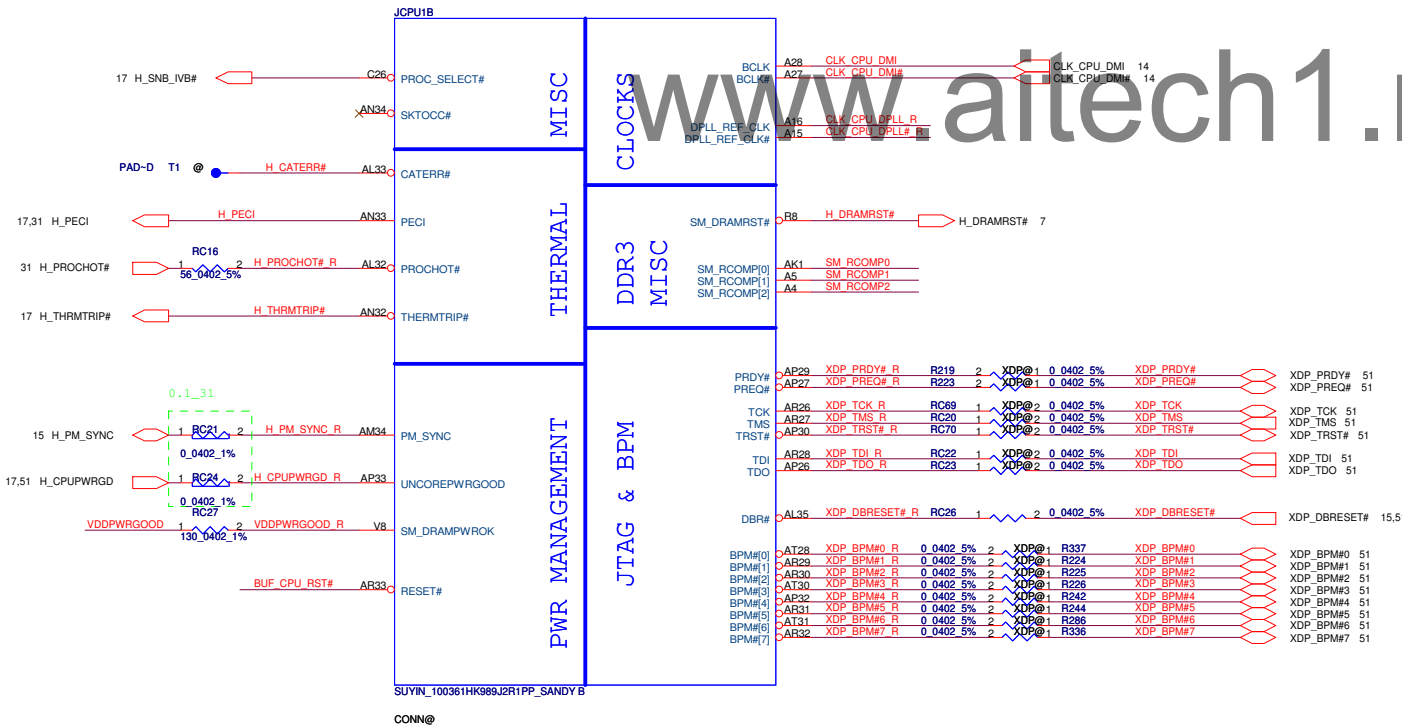
| STATE | SIGNAL | SLP_S3# | SLP_S4# | SLP_S5# | +VALW | +VS |
|-----------------------|--------|---------|---------|---------|-------|-----|
| Full ON | | HIGH | HIGH | HIGH | ON | ON |
| S1 (Power On Suspend) | | HIGH | HIGH | HIGH | ON | ON |
| S3 (Suspend to RAM) | | LOW | HIGH | HIGH | ON | OFF |
| S4 (Suspend to Disk) | | LOW | LOW | HIGH | ON | OFF |
| S5 (Soft OFF) | | LOW | LOW | LOW | ON | OFF |

| EC SM Bus2 Address | | | |
|--------------------|--------|-----|---------|
| Power | Device | HEX | Address |
| | | | |



RC4
CRB 1.1K
CHECK LIST 0.7 --> 4.75K
INTEL recommend 1.1K
PDG 0.71 rev -->200

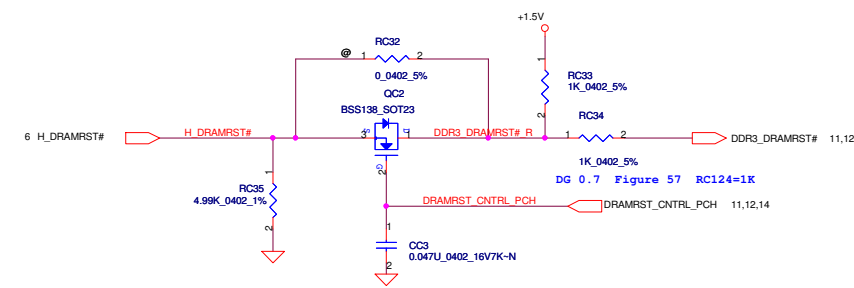
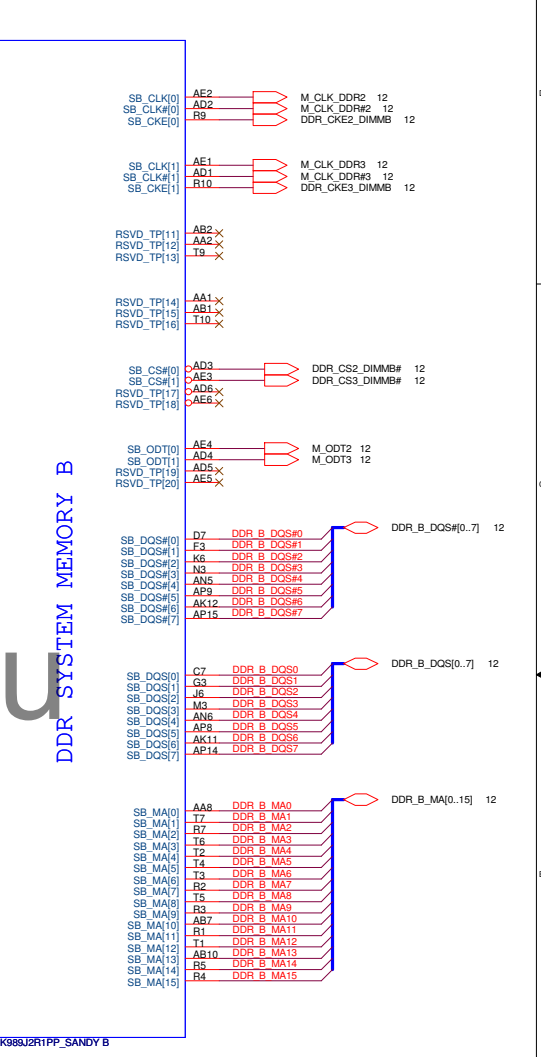
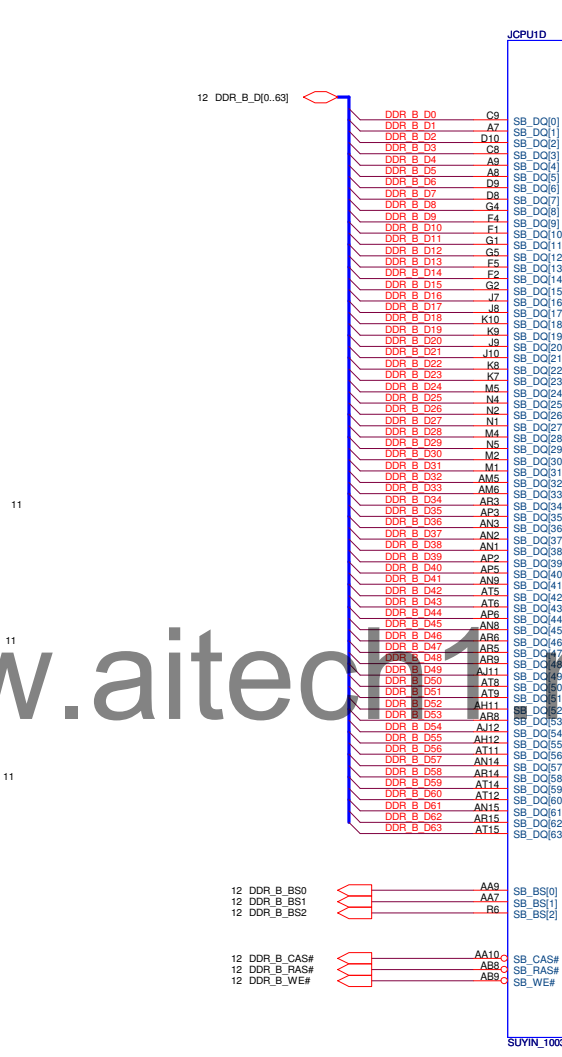
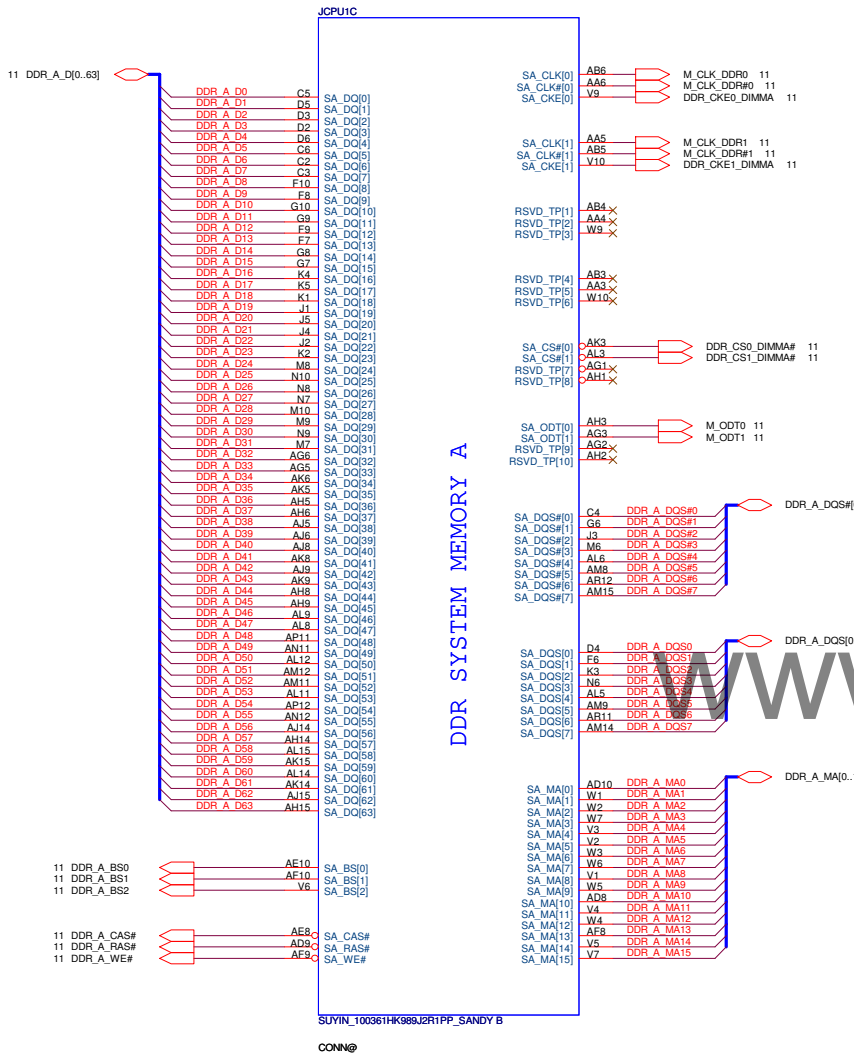
INTEL Check list



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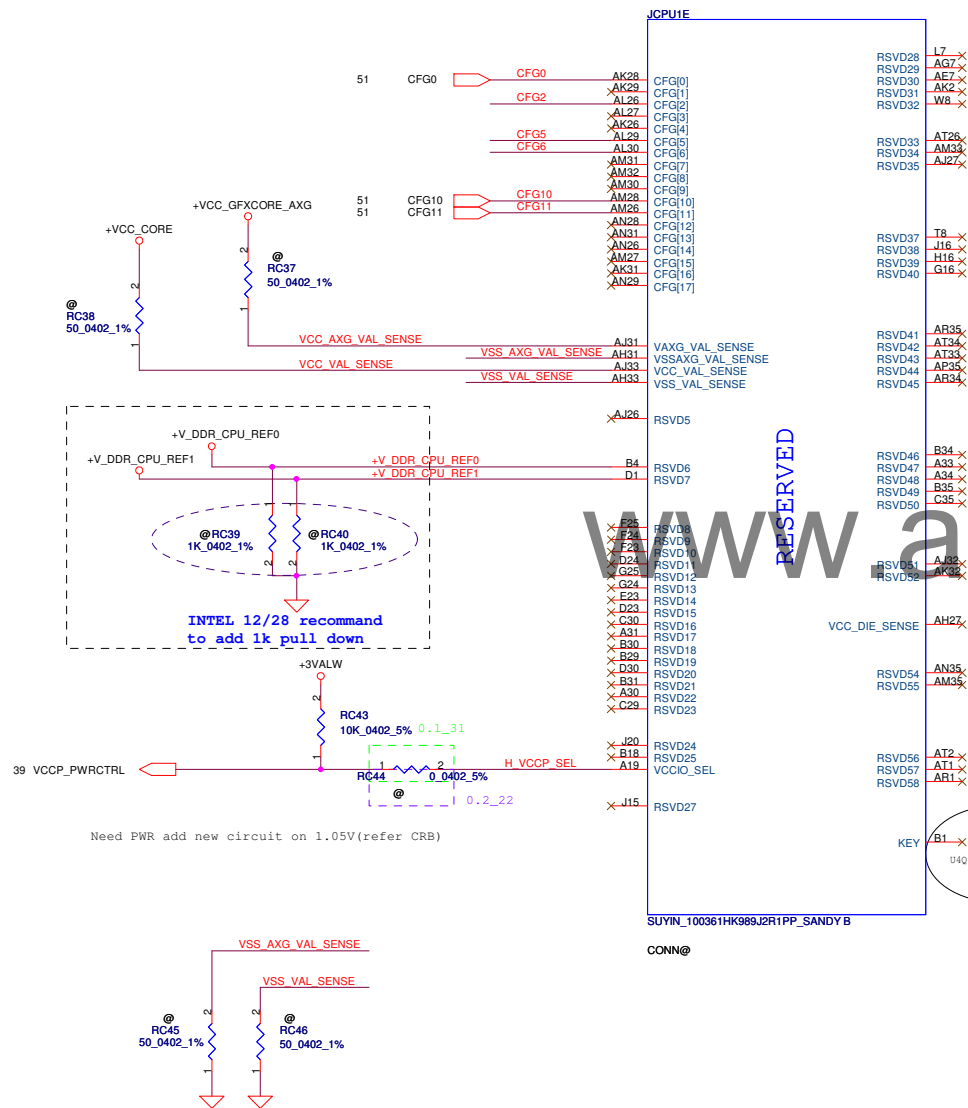
PROCESSOR(2/6) PM,XDP,CLK



| | | | | | |
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| | | | | PROCESSOR(3/6) DDRIII | |
| | | | | Size | Document Number |
| | | | | Customer | Rev |
| | | | | LA-835IP | |
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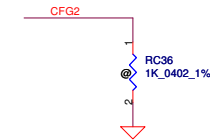
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PROCESSOR(3/6) DDRIII

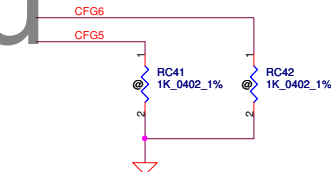


INTEL 12/28 recommend
to add RC120, RC121, RC122, RC123
Please place as close as JCPU1

CFG Straps for Processor

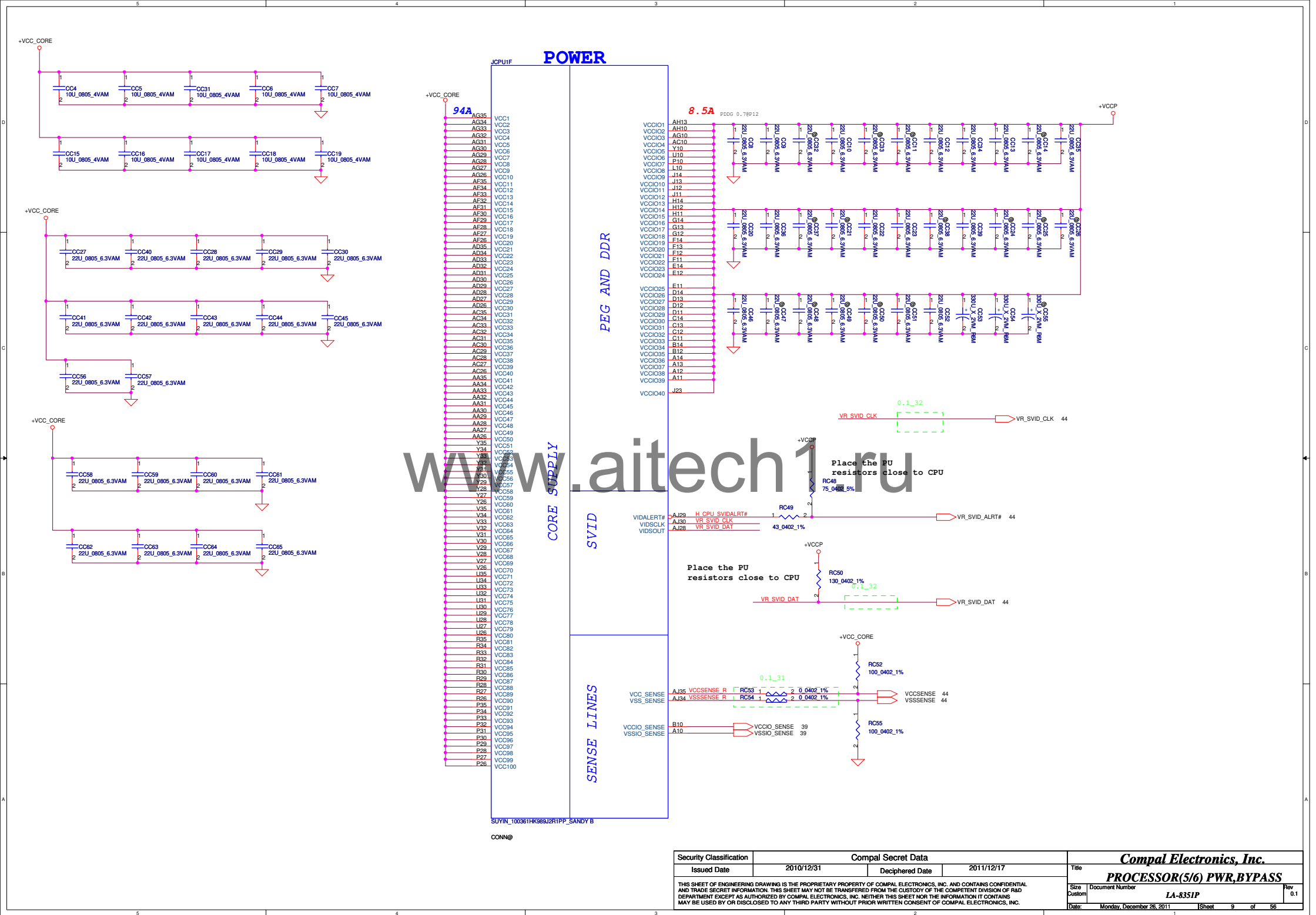


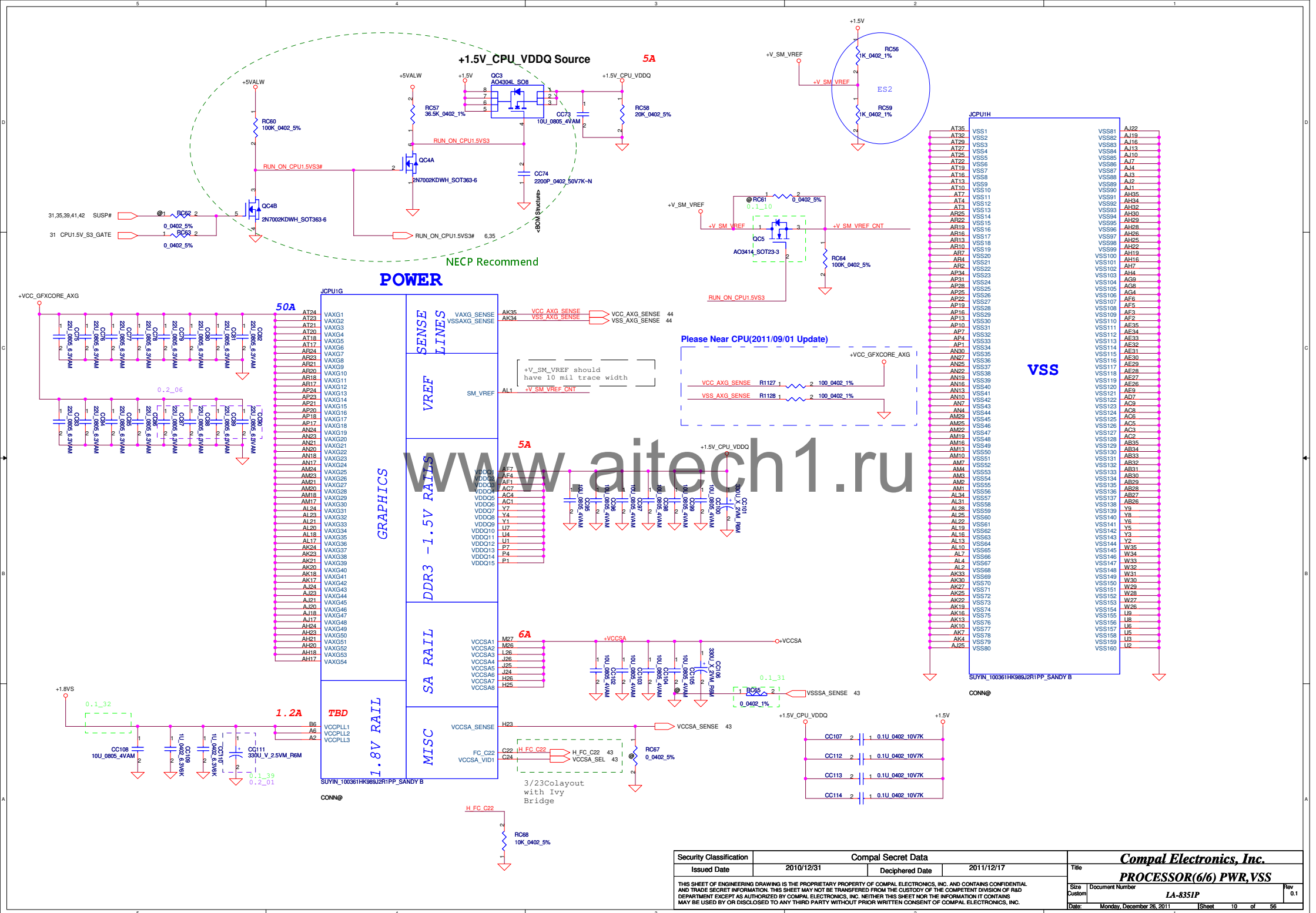
| PEG Static Lane Reversal - CFG2 is for the 16x | |
|--|---|
| CFG2 | 1:(Default) Normal Operation; Lane # definition matches socket pin map definition 0:Lane Reversed |

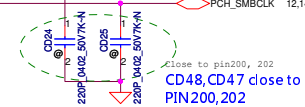
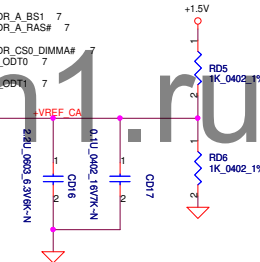
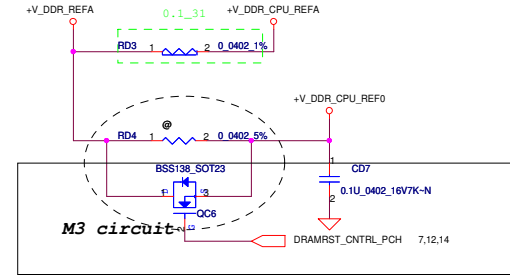
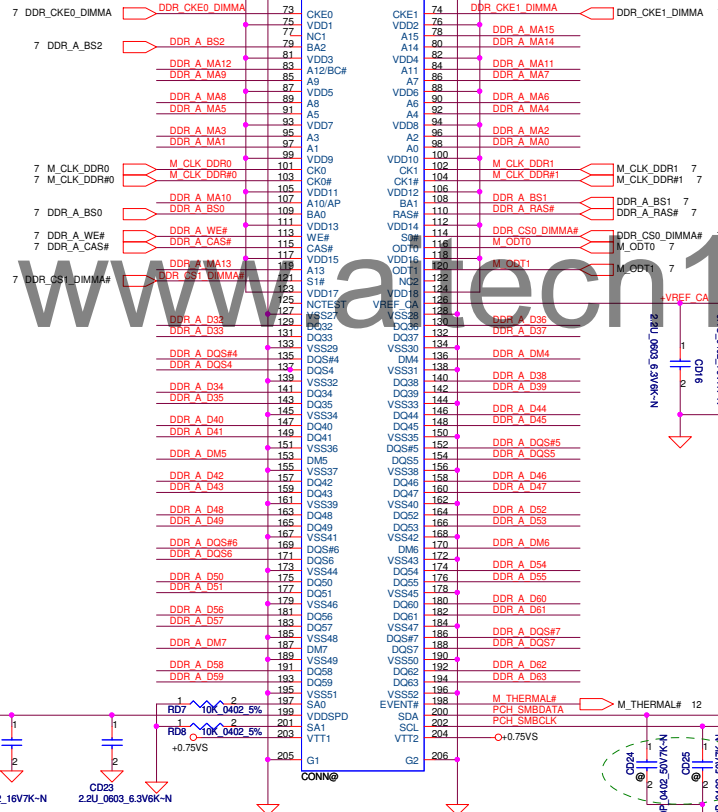
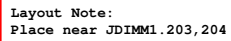
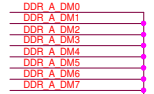
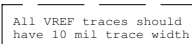


| PCIe Port Bifurcation Straps | |
|------------------------------|--|
| CFG[6:5] | 11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled |

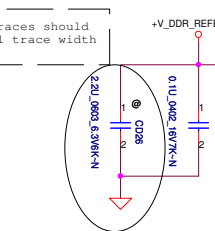
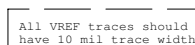
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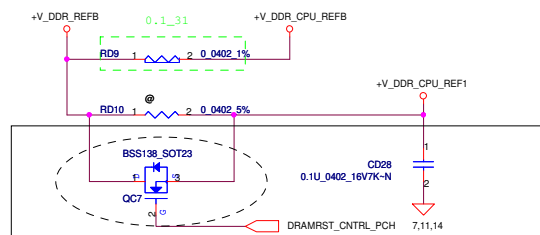
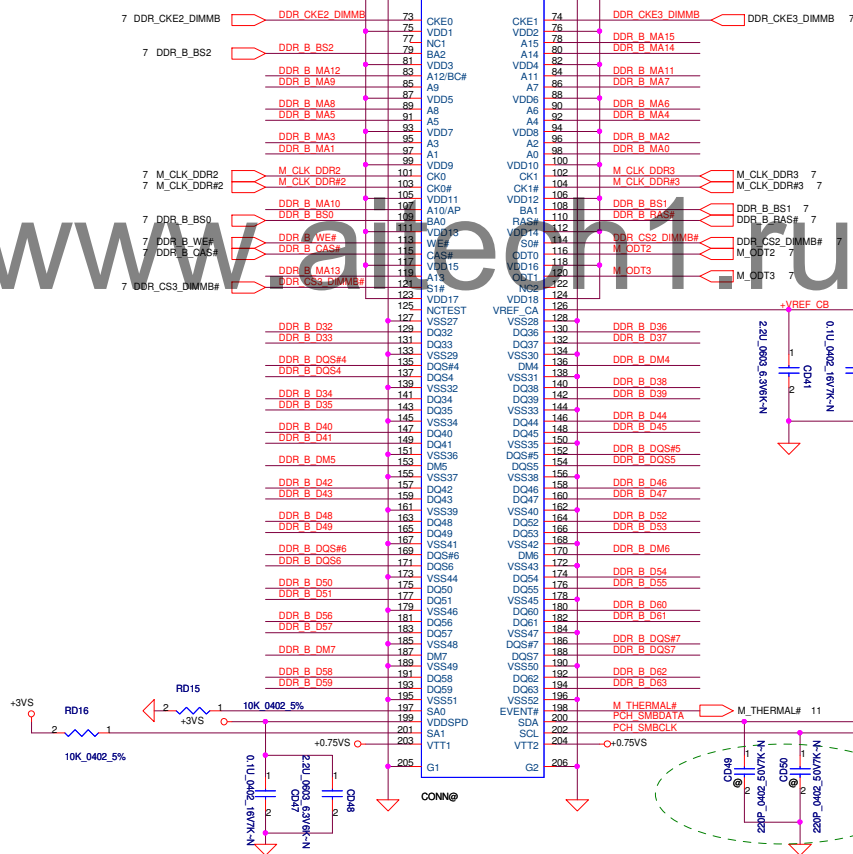
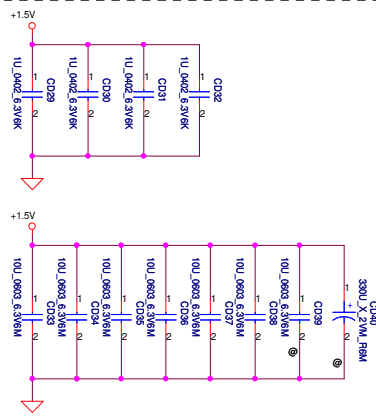




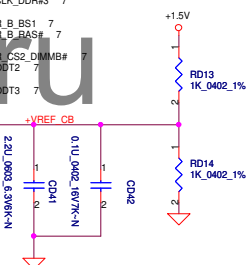
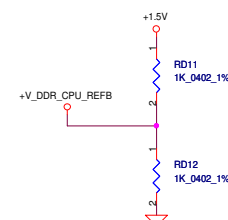
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DDR B DM0
DDR B DM1
DDR B DM2
DDR B DM3
DDR B DM4
DDR B DM6
DDR B DM7

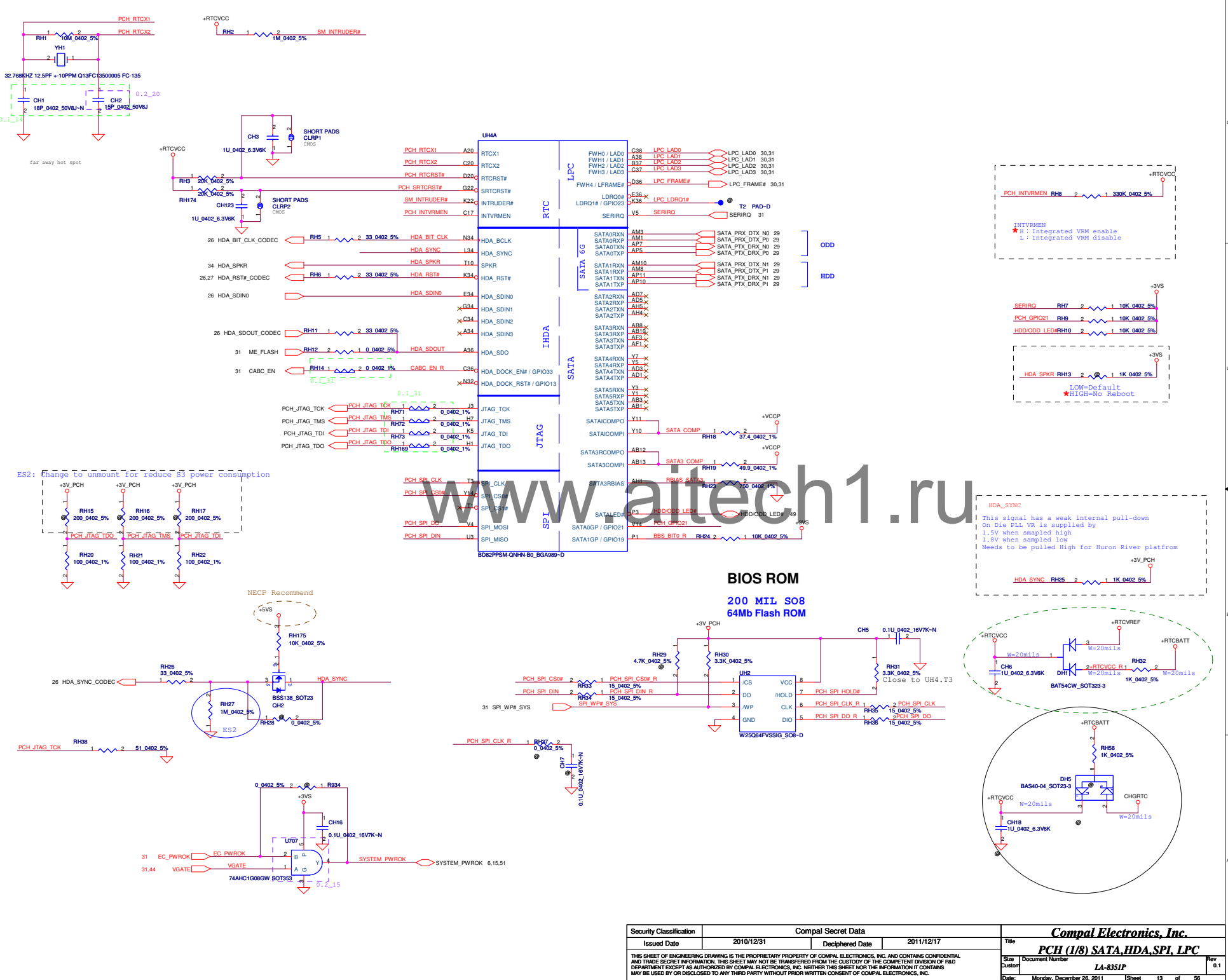


M3 circuit

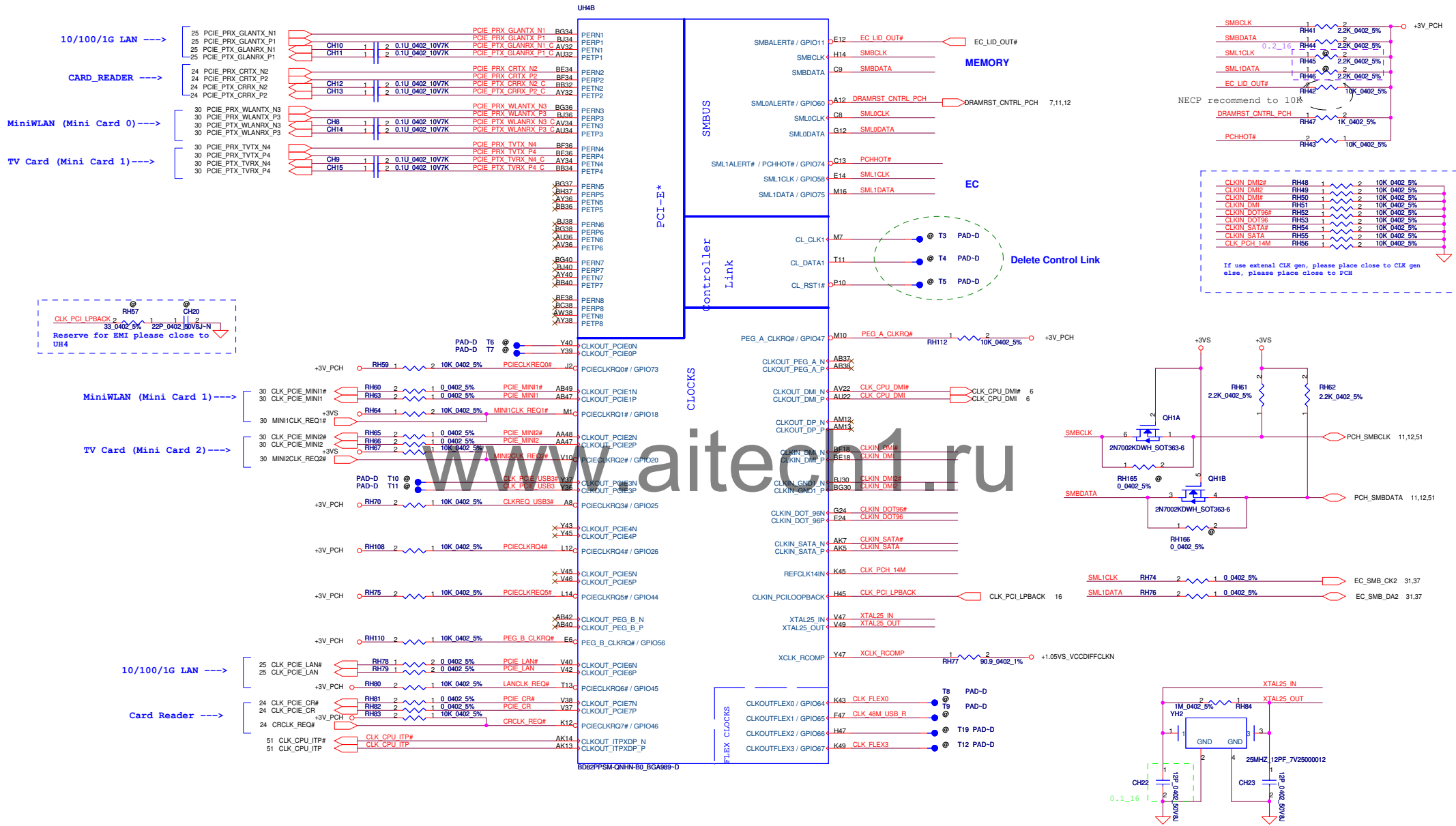


CD49, CD 50 close to
PIN 200, 202

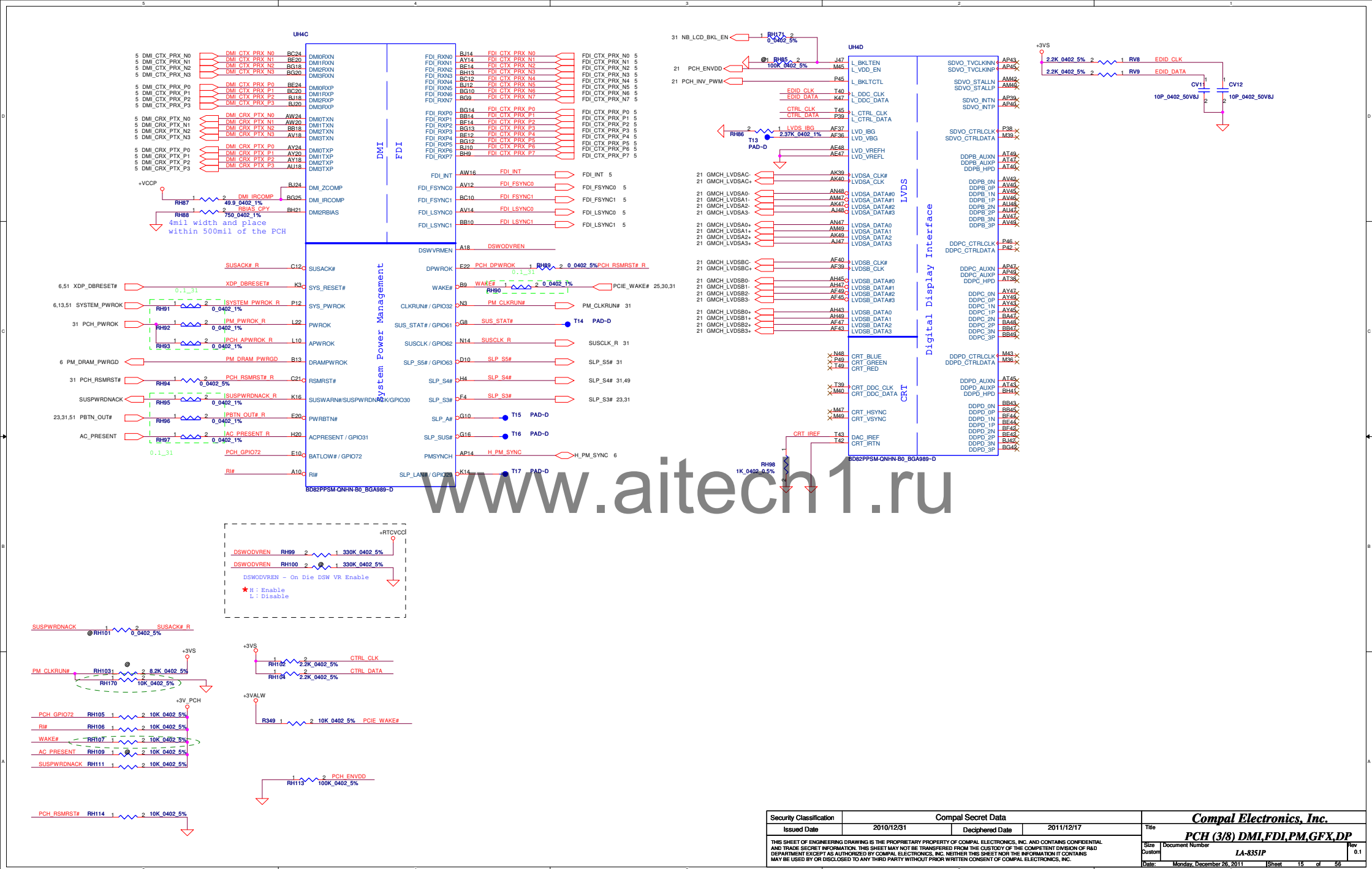
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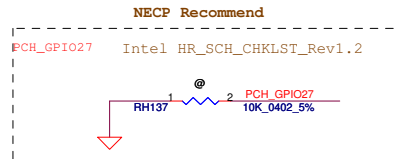
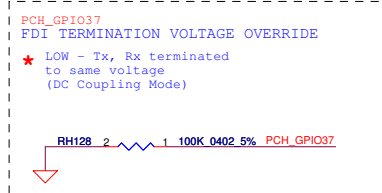
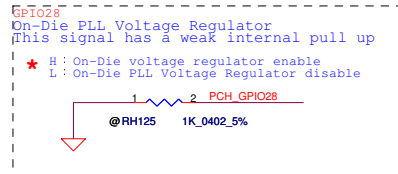


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| | | | | Custom | LA-8351P |
| | | | | Date | Monday, December 26, 2011 |
| | | | | Sheet | 13 of 56 |

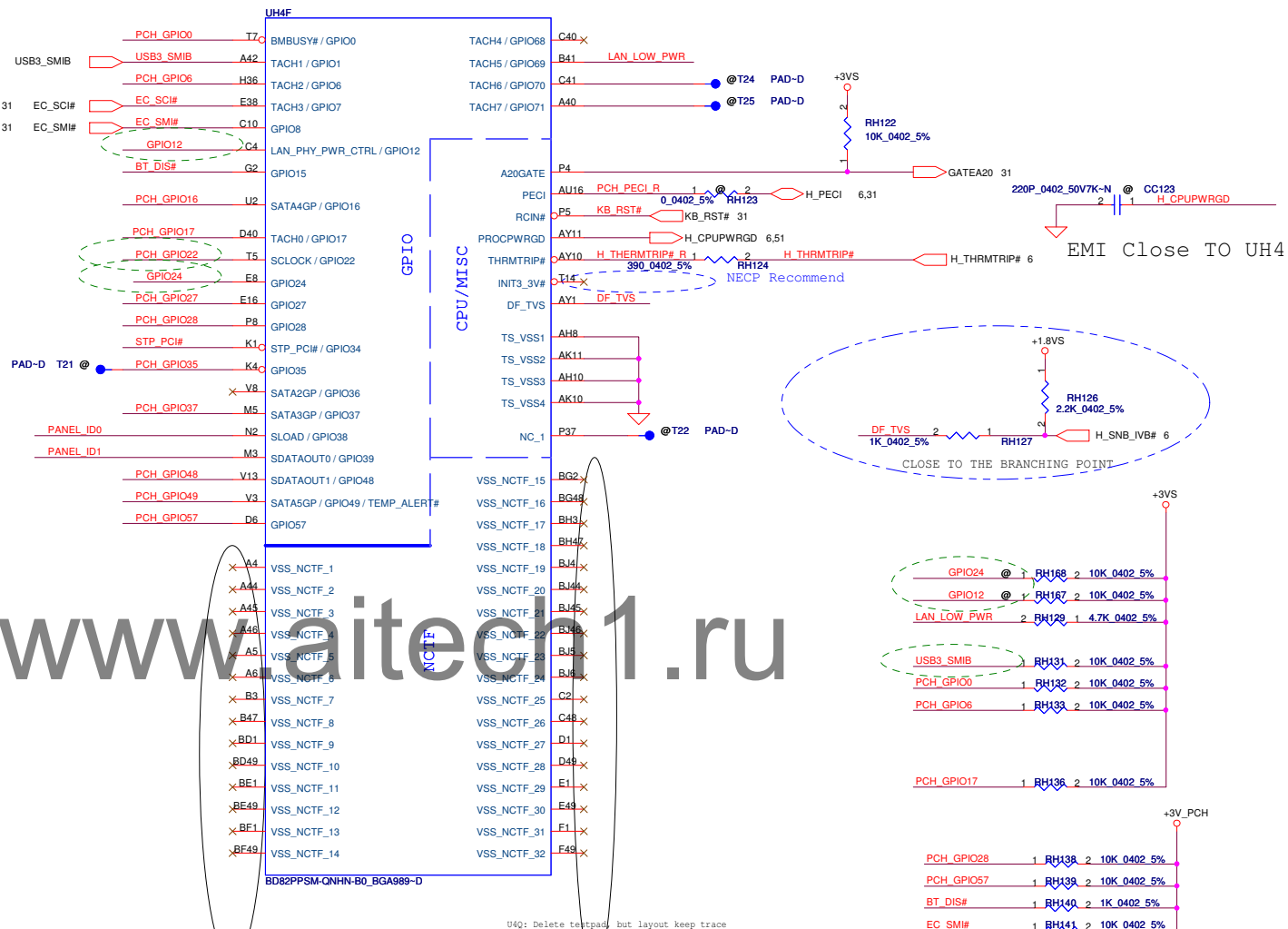
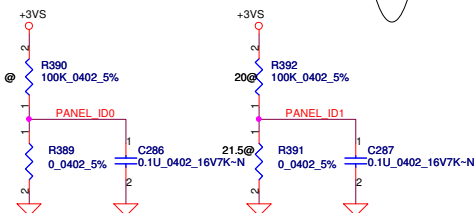


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| Date | | Monday, December 26, 2011 | | Sheet 14 of 56 | |

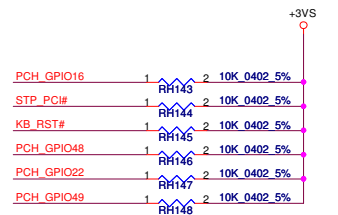




| PANEL_ID0 | PANEL_ID1 | Panel |
|-----------|-----------|--------------------|
| 0 | 0 | LG LM215WF3 |
| 0 | 1 | Samsung LTM200KT10 |
| 1 | 0 | |
| 1 | 1 | |

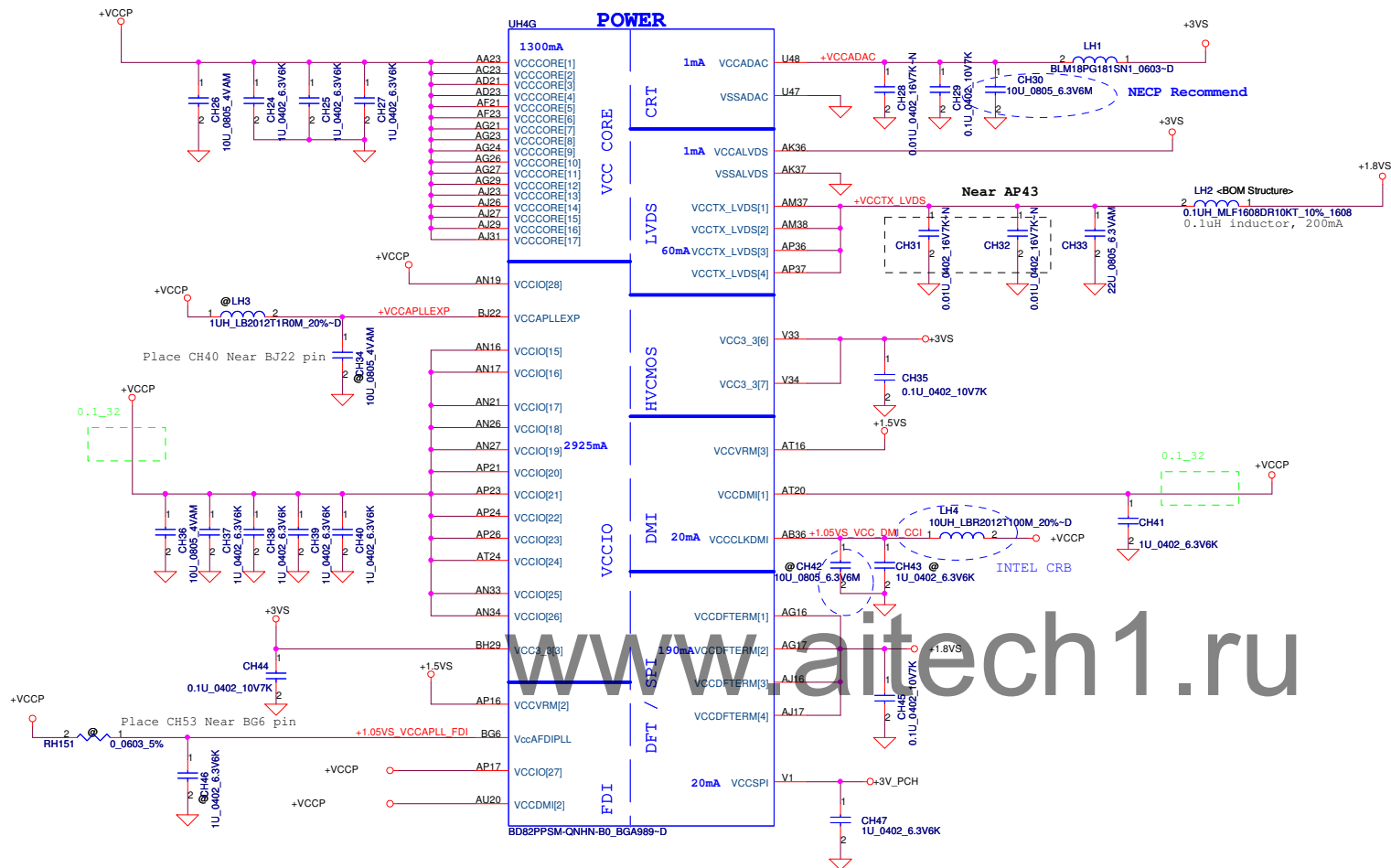


U40: Delete testpad, but layout keep trace



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|-------------------------|--|--|--|--------------------|--|--|--|---------------------------------|--|--|--|
| Issued Date | | | | 2010/12/31 | | | | Title | | | |
| | | | | Deciphered Date | | | | PCH (5/8) GPIO, CPU, MISC | | | |
| | | | | 2011/12/17 | | | | Size | | | |
| | | | | | | | | Custom | | | |
| | | | | | | | | Document Number | | | |
| | | | | | | | | LA-8351P | | | |
| | | | | | | | | Rev | | | |
| | | | | | | | | 0.1 | | | |
| | | | | | | | | Date: Monday, December 26, 2011 | | | |
| | | | | | | | | Sheet 17 of 56 | | | |

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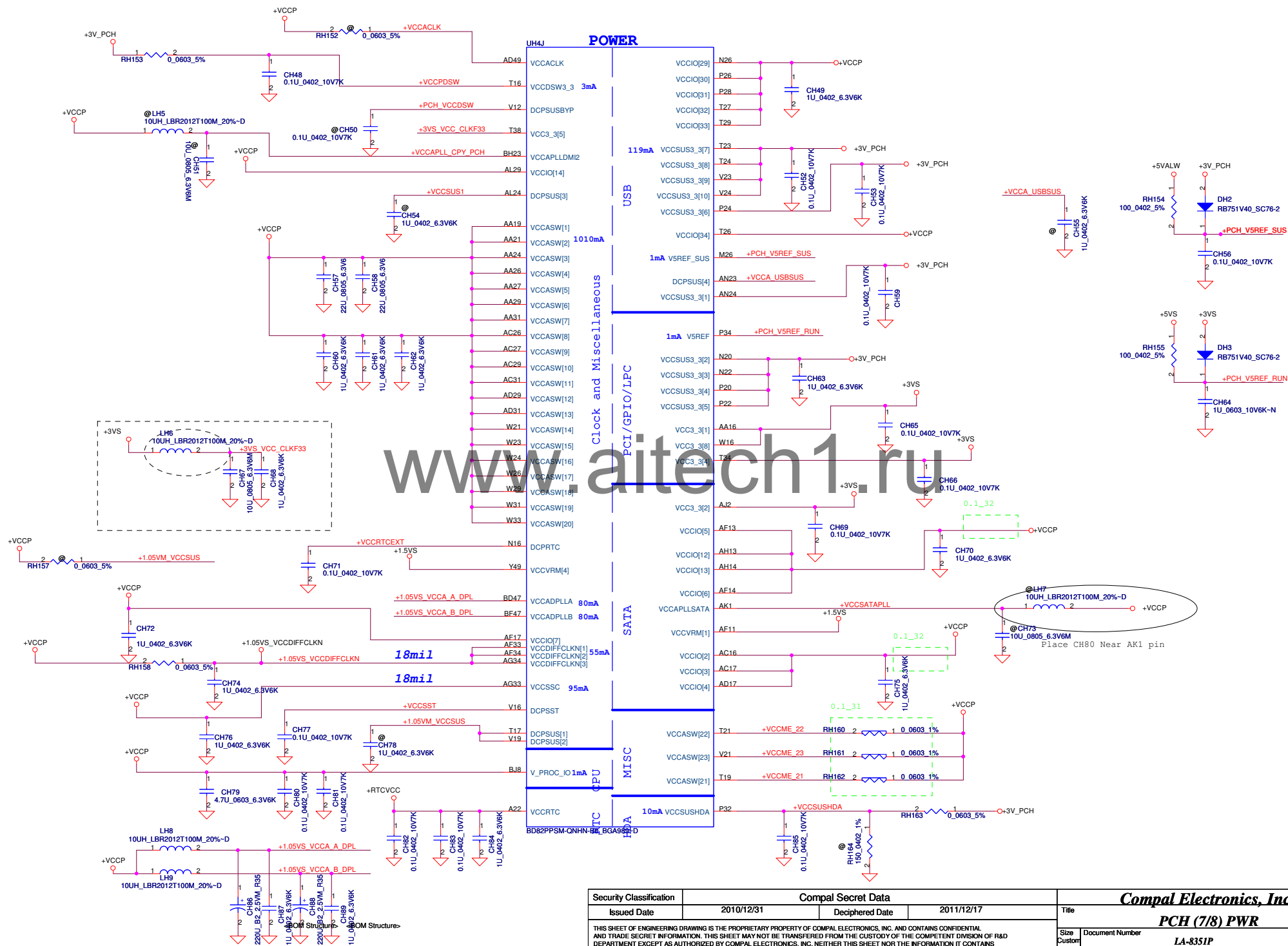


| PCH Power Rail Table | | |
|----------------------|-----------|-----------------------|
| Voltage Rail | Voltage | SO Iccmax Current (A) |
| V_PROC_IO | 1.05 | 0.001 |
| V5REF | 5 | 0.001 |
| V5REF_Sus | 5 | 0.001 |
| Vcc3_3 | 3.3 | 0.266 |
| VccADAC | 3.3 | 0.001 |
| VccADPLLA | 1.05 | 0.08 |
| VccADPLLB | 1.05 | 0.08 |
| VccCore | 1.05 | 1.3 |
| VccDMI | 1.05 | 0.042 |
| VccIO | 1.05 | 2.925 |
| VccASW | 1.05 | 1.01 |
| VccSPI | 3.3 | 0.02 |
| VccDSW | 3.3 | 0.003 |
| VccpNAND | 1.8 | 0.19 |
| VccRTC | 3.3 | 6 uA |
| VccSus3_3 | 3.3 | 0.119 |
| VccSusHDA | 3.3 / 1.5 | 0.01 |
| VccVRM | 1.8 / 1.5 | 0.16 |
| VccCLKDMI | 1.05 | 0.02 |
| VccSSC | 1.05 | 0.095 |
| VccDIFFCLKN | 1.05 | 0.055 |
| VccALVDS | 3.3 | 0.001 |
| VccTX_LVDS | 1.8 | 0.06 |

VCCVRM = 160mA detal waiting for newest spec

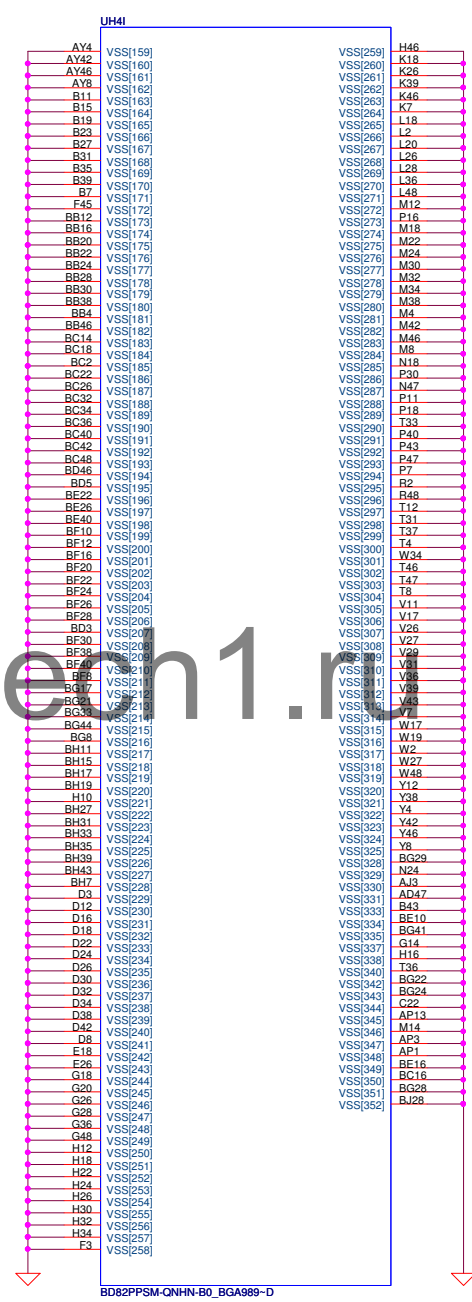
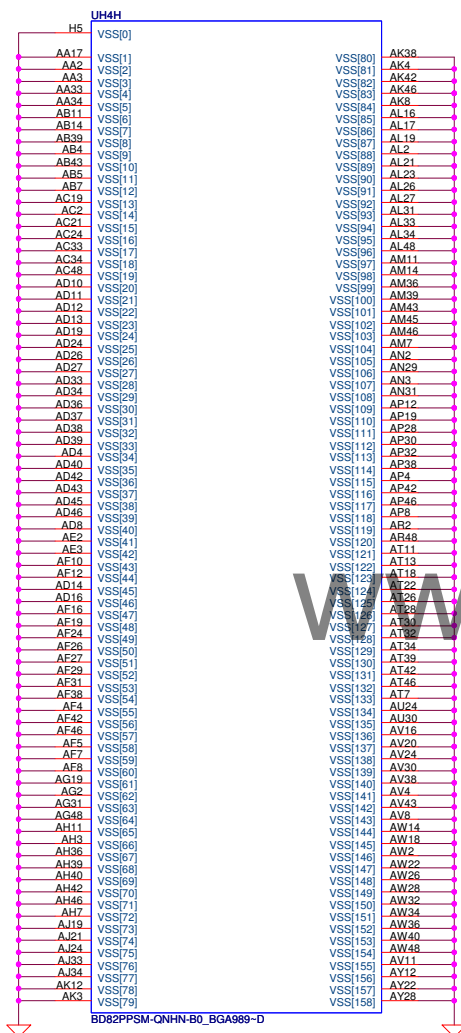
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|---|------------|--------------------|------------|--------------------------|---------------------------|
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| | | | | Rev | 0.1 |
| | | | | Date: | Monday, December 26, 2011 |
| | | | | Sheet | 18 of 56 |

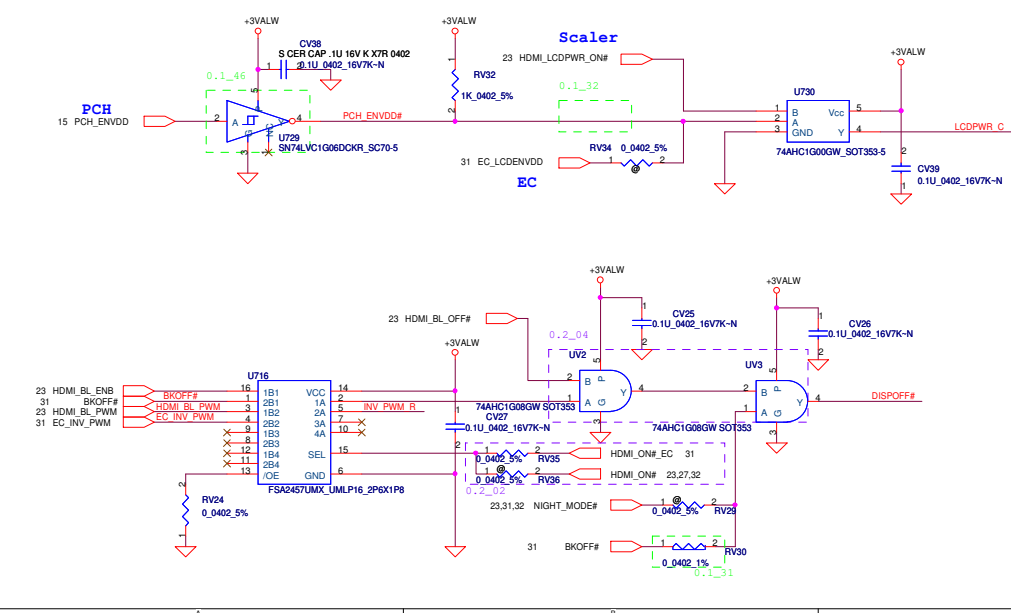
VCC3_3 = 266mA detal waiting for newest spec
VCCDMI = 42mA detal waiting for newest spec



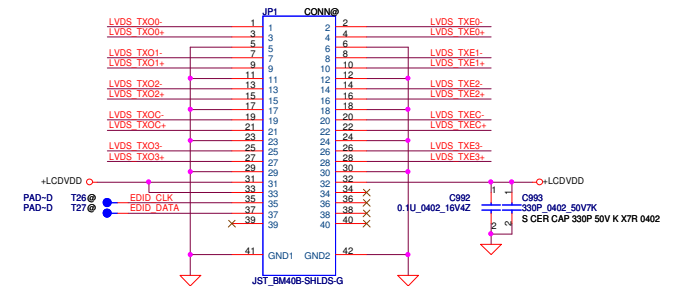
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|---|--------------------|-----------------|------------|
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| Compal Electronics, Inc. | | | |
|--------------------------|---------------------------|-----------------|----------|
| PCH (7/8) PWR | | | |
| Title | Size | Document Number | Rev |
| | Custom | LA-8351P | 0.1 |
| Date: | Monday, December 26, 2011 | Sheet | 19 of 56 |

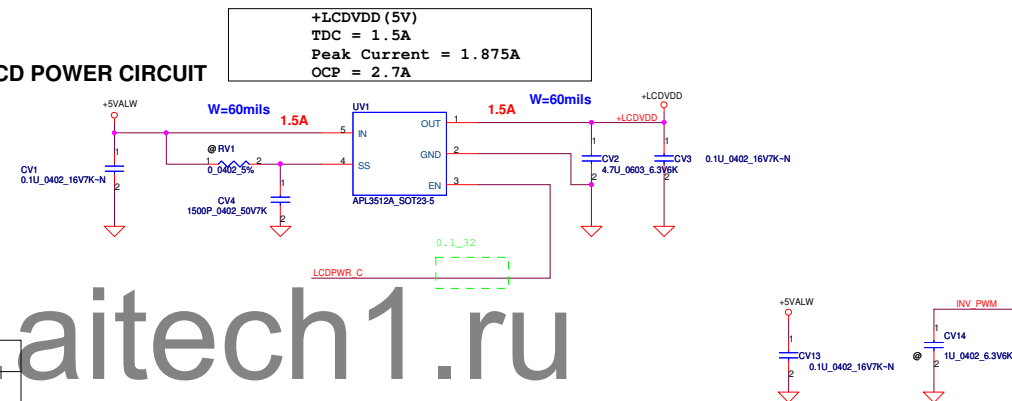




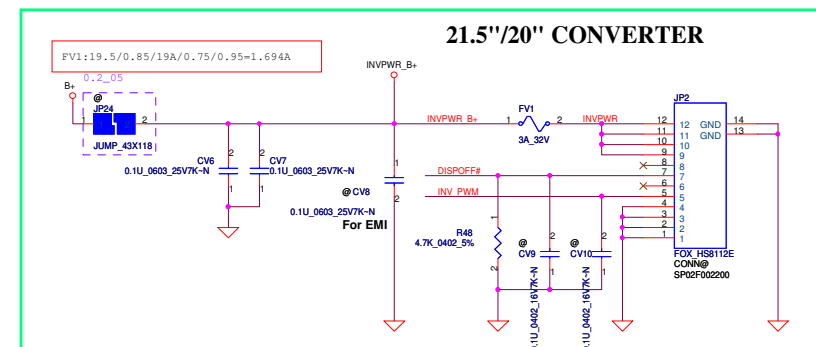
For LVDS Conn.

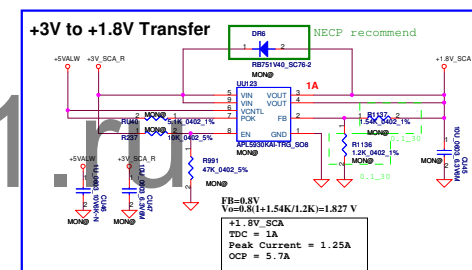
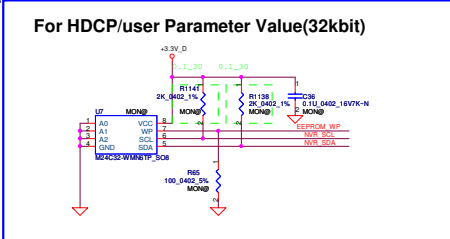
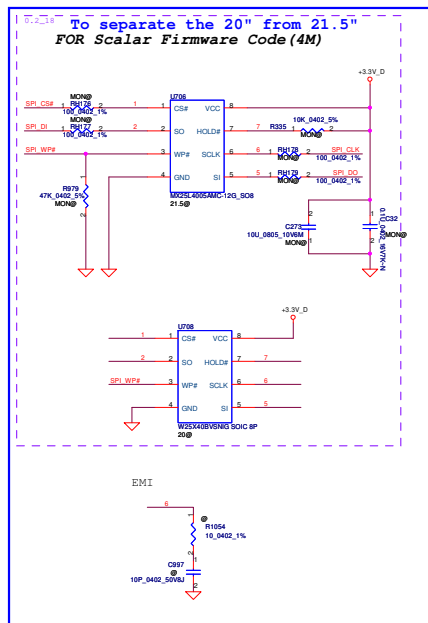
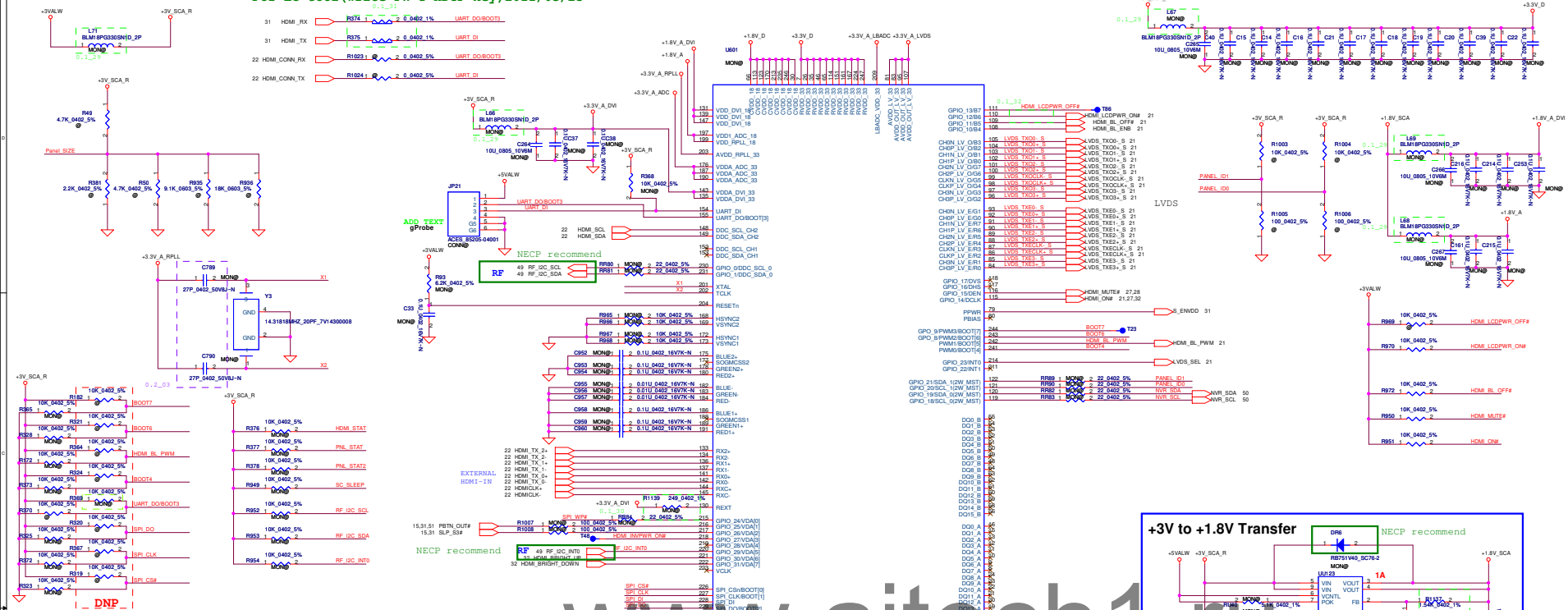


LCD POWER CIRCUIT

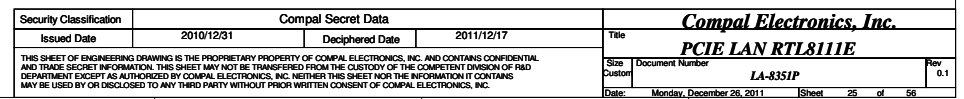
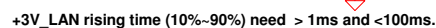
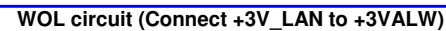
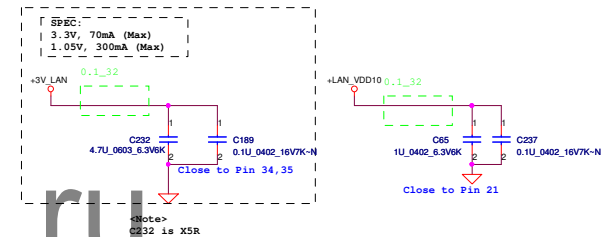


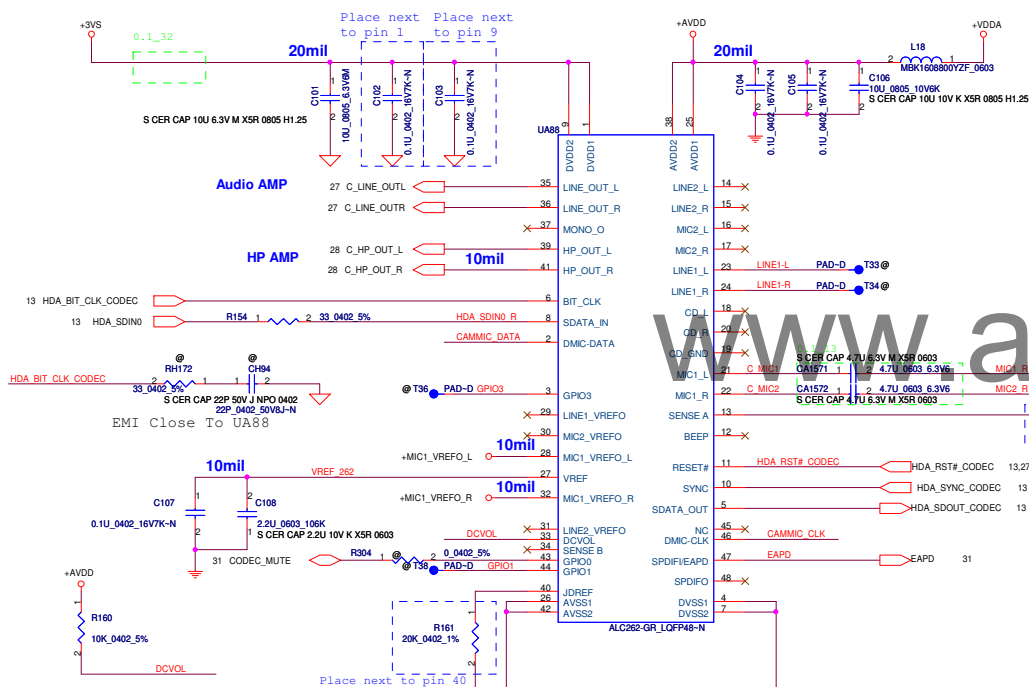
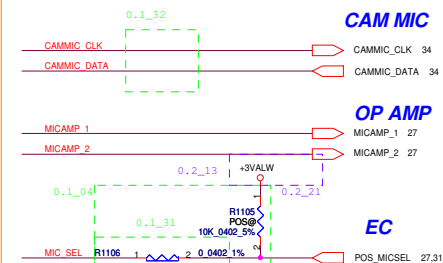
| SEL | Function |
|-----|----------|
| L | nB1 |
| H | nB2 |



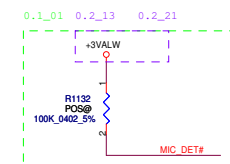


| | | | | | |
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| | | | | Doc. No. LA-8351P | |
| Rev. Monday, December 26, 2011 | | Rev. | | Page 25 of 55 | |





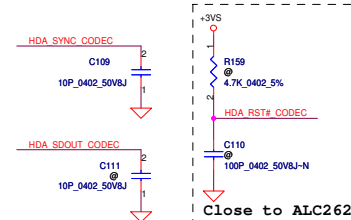
| PC state | Min in | U735(Mic out selector) out |
|----------|--------|----------------------------|
| S0 | Enable | to codec |
| | N/A | to codec |
| S3 | Enable | to speaker |
| | N/A | to codec |
| S4 | Enable | to speaker |
| | N/A | to codec |
| S5 | Enable | to speaker |
| | N/A | to codec |



Power Off Speaker (2011/09/02)

The diagram illustrates the electrical connections for a microphone and a speaker. Key components and their connections include:

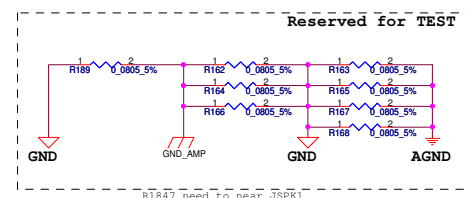
- Microphone (MIC1, MIC2):** Connected to MIC1_CODEC and MIC2_CODEC. MIC1 is also connected to MIC_ID# and MIC_DET#.
- Speaker (L16, L17):** Connected to MIC1_CODEC and MIC2_CODEC. L16 is connected to MIC1_CODEC and MIC2_CODEC. L17 is connected to MIC1_CODEC and MIC2_CODEC.
- Power and Ground:** Connected to 220P_0402_50V7K-N and 220P_0402_50V7K-N.
- Other Components:** C99, C100, C1030, C1031, C1032, C1033, C1034, C1035, C1036, C1037, C1038, C1039, C1040, C1041, C1042, C1043, C1044, C1045, C1046, C1047, C1048, C1049, C1050, C1051, C1052, C1053, C1054, C1055, C1056, C1057, C1058, C1059, C1060, C1061, C1062, C1063, C1064, C1065, C1066, C1067, C1068, C1069, C1070, C1071, C1072, C1073, C1074, C1075, C1076, C1077, C1078, C1079, C1080, C1081, C1082, C1083, C1084, C1085, C1086, C1087, C1088, C1089, C1090, C1091, C1092, C1093, C1094, C1095, C1096, C1097, C1098, C1099, C1100, C1101, C1102, C1103, C1104, C1105, C1106, C1107, C1108, C1109, C1110, C1111, C1112, C1113, C1114, C1115, C1116, C1117, C1118, C1119, C1120, C1121, C1122, C1123, C1124, C1125, C1126, C1127, C1128, C1129, C1130, C1131, C1132, C1133, C1134, C1135, C1136, C1137, C1138, C1139, C1140, C1141, C1142, C1143, C1144, C1145, C1146, C1147, C1148, C1149, C1150, C1151, C1152, C1153, C1154, C1155, C1156, C1157, C1158, C1159, C1160, C1161, C1162, C1163, C1164, C1165, C1166, C1167, C1168, C1169, C1170, C1171, C1172, C1173, C1174, C1175, C1176, C1177, C1178, C1179, C1180, C1181, C1182, C1183, C1184, C1185, C1186, C1187, C1188, C1189, C1190, C1191, C1192, C1193, C1194, C1195, C1196, C1197, C1198, C1199, C1200, C1201, C1202, C1203, C1204, C1205, C1206, C1207, C1208, C1209, C1210, C1211, C1212, C1213, C1214, C1215, C1216, C1217, C1218, C1219, C1220, C1221, C1222, C1223, C1224, C1225, C1226, C1227, C1228, C1229, C1230, C1231, C1232, C1233, C1234, C1235, C1236, C1237, C1238, C1239, C1240, C1241, C1242, C1243, C1244, C1245, C1246, C1247, C1248, C1249, C1250, C1251, C1252, C1253, C1254, C1255, C1256, C1257, C1258, C1259, C1260, C1261, C1262, C1263, C1264, C1265, C1266, C1267, C1268, C1269, C1270, C1271, C1272, C1273, C1274, C1275, C1276, C1277, C1278, C1279, C1280, C1281, C1282, C1283, C1284, C1285, C1286, C1287, C1288, C1289, C1290, C1291, C1292, C1293, C1294, C1295, C1296, C1297, C1298, C1299, C1300, C1301, C1302, C1303, C1304, C1305, C1306, C1307, C1308, C1309, C1310, C1311, C1312, C1313, C1314, C1315, C1316, C1317, C1318, C1319, C1320, C1321, C1322, C1323, C1324, C1325, C1326, C1327, C1328, C1329, C1330, C1331, C1332, C1333, C1334, C1335, C1336, C1337, C1338, C1339, C1340, C1341, C1342, C1343, C1344, C1345, C1346, C1347, C1348, C1349, C1350, C1351, C1352, C1353, C1354, C1355, C1356, C1357, C1358, C1359, C1360, C1361, C1362, C1363, C1364, C1365, C1366, C1367, C1368, C1369, C1370, C1371, C1372, C1373, C1374, C1375, C1376, C1377, C1378, C1379, C1380, C1381, C1382, C1383, C1384, C1385, C1386, C1387, C1388, C1389, C1390, C1391, C1392, C1393, C1394, C1395, C1396, C1397, C1398, C1399, C1400, C1401, C1402, C1403, C1404, C1405, C1406, C1407, C1408, C1409, C1410, C1411, C1412, C1413, C1414, C1415, C1416, C1417, C1418, C1419, C1420, C1421, C1422, C1423, C1424, C1425, C1426, C1427, C1428, C1429, C1430, C1431, C1432, C1433, C1434, C1435, C1436, C1437, C1438, C1439, C1440, C1441, C1442, C1443, C1444, C1445, C1446, C1447, C1448, C1449, C1450, C1451, C1452, C1453, C1454, C1455, C1456, C1457, C1458, C1459, C1460, C1461, C1462, C1463, C1464, C1465, C1466, C1467, C1468, C1469, C1470, C1471, C1472, C1473, C1474, C1475, C1476, C1477, C1478, C1479, C1480, C1481, C1482, C1483, C1484, C1485, C1486, C1487, C1488, C1489, C1490, C1491, C1492, C1493, C1494, C1495, C1496, C1497, C1498, C1499, C1500, C1501, C1502, C1503, C1504, C1505, C1506, C1507, C1508, C1509, C1510, C1511, C1512, C1513, C1514, C1515, C1516, C1517, C1518, C1519, C1520, C1521, C1522, C1523, C1524, C1525, C1526, C1527, C1528, C1529, C1530, C1531, C1532, C1533, C1534, C1535, C1536, C1537, C1538, C1539, C1540, C1541, C1542, C1543, C1544, C1545, C1546, C1547, C1548, C1549, C1550, C1551, C1552, C1553, C1554, C1555, C1556, C1557, C1558, C1559, C1560, C1561, C1562, C1563, C1564, C1565, C1566, C1567, C1568, C1569, C1570, C1571, C1572, C1573, C1574, C1575, C1576, C1577, C1578, C1579, C1580, C1581, C1582, C1583, C1584, C1585, C1586, C1587, C1588, C1589, C1590, C1591, C1592, C1593, C1594, C1595, C1596, C1597, C1598, C1599, C1600, C1601, C1602, C1603, C1604, C1605, C1606, C1607, C1608, C1609, C1610, C1611, C1612, C1613, C1614, C1615, C1616, C1617, C1618, C1619, C1620, C1621, C1622, C1623, C1624, C1625, C1626, C1627, C1628, C1629, C1630, C1631, C1632, C1633, C1634, C1635, C1636, C1637, C1638, C1639, C1640, C1641, C1642, C1643, C1644, C1645, C1646, C1647, C1648, C1649, C1650, C1651, C1652, C1653, C1654, C1655, C1656, C1657, C1658, C1659, C1660, C1661, C1662, C1663, C1664, C1665, C1666, C1667, C1668, C1



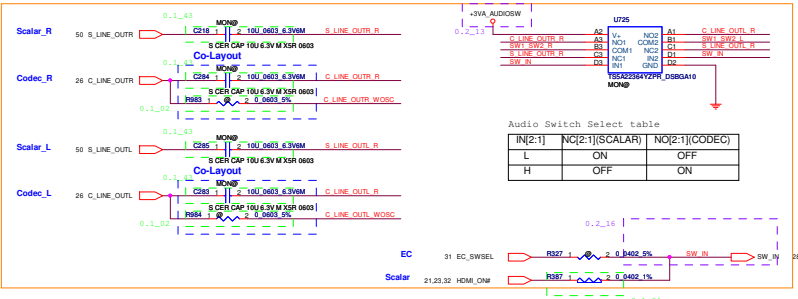
3 external jacks (Line-in / Mic-in / Hp-out + Spdif out
combo) / internal speaker / internal subwoofer / internal
mic(digital or analog mic supporting)

| Pin Assignment | Location | Function |
|----------------------|----------|---|
| HP-OUT (pin-39/41) | External | Headphone out w/ amplifier |
| LINE-OUT (pin-35/36) | Internal | Internal Speaker |
| LINE1 (pin-23/24) | External | Line in |
| MIC1 (pin-21/22) | External | Mic in |
| MONO-OUT (pin-37) | Internal | Internal Subwoofer |
| MIC2 (pin-16/17) | Internal | Internal Mic (Analog MIC / Analog Mic Array -- need stereo mic) |
| DMIC (pin-2/46) | Internal | Internal Mic (Digital MIC / Digital Mic Array -- need stereo mic) |

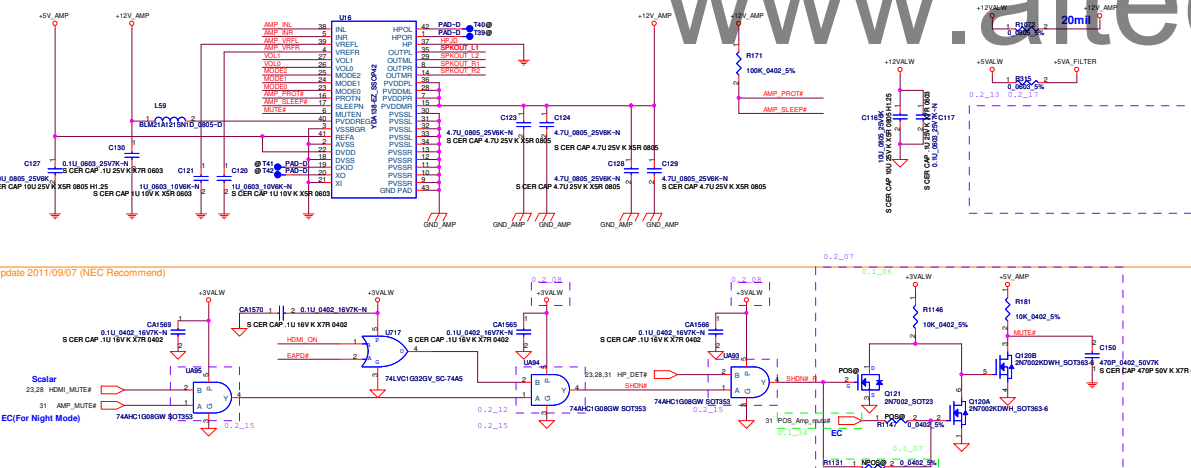
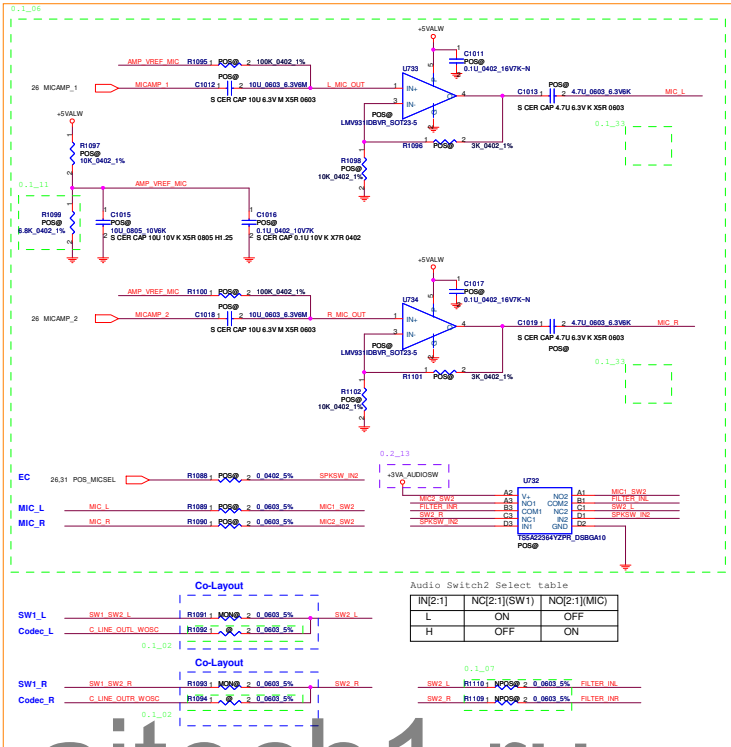
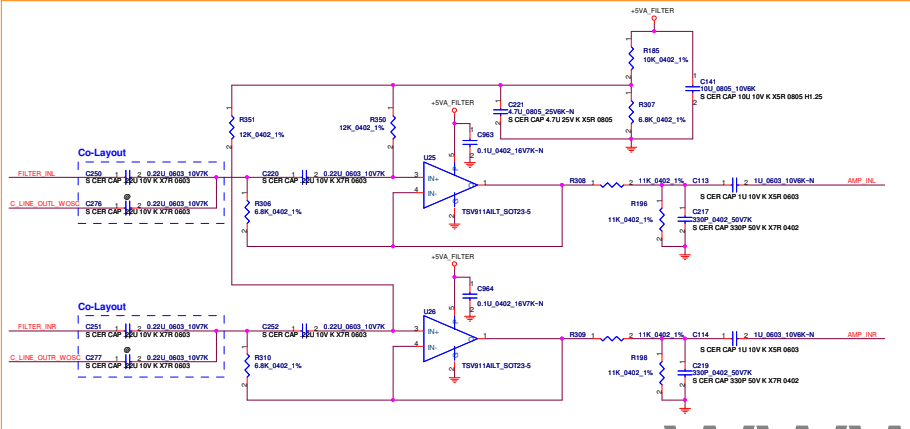
PhoneJacks are all normally open type.



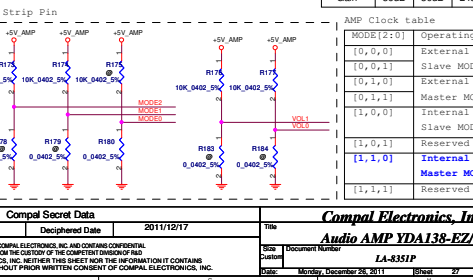
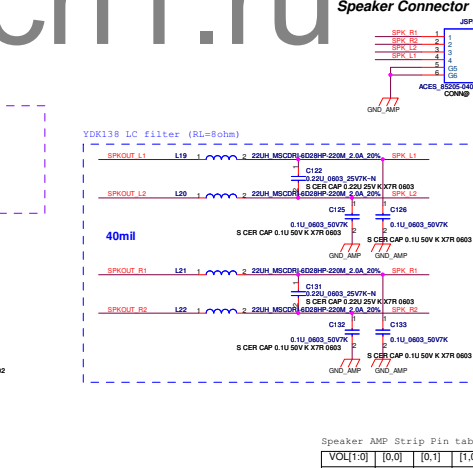
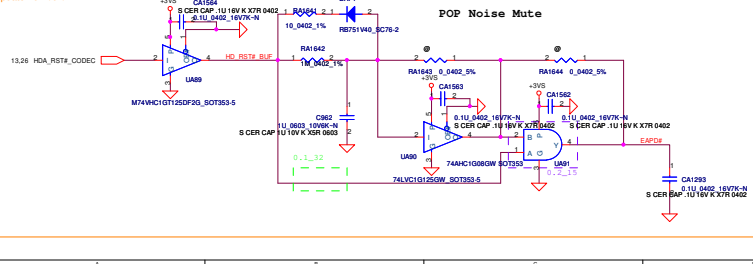
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| Security Classification | Compal Secret Data | | | Compal Electronics, Inc. | | |
| Issued Date | 2010/12/31 | Deciphered Date | 2011/12/17 | Title | HD Audio ALC262/Mic Jack | |
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| | | | | | LA-835IP | 0 |
| Date: | | | | Monday, December 26, 2011 | Sheet | 26 of 56 |

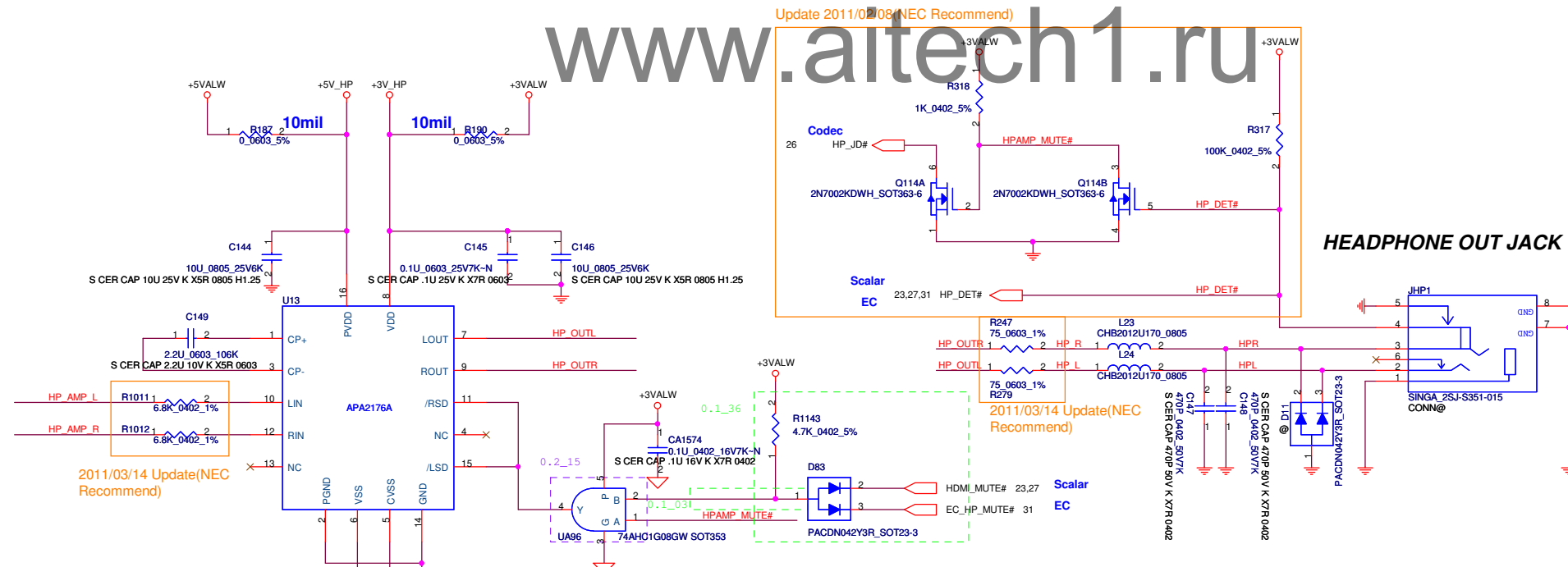
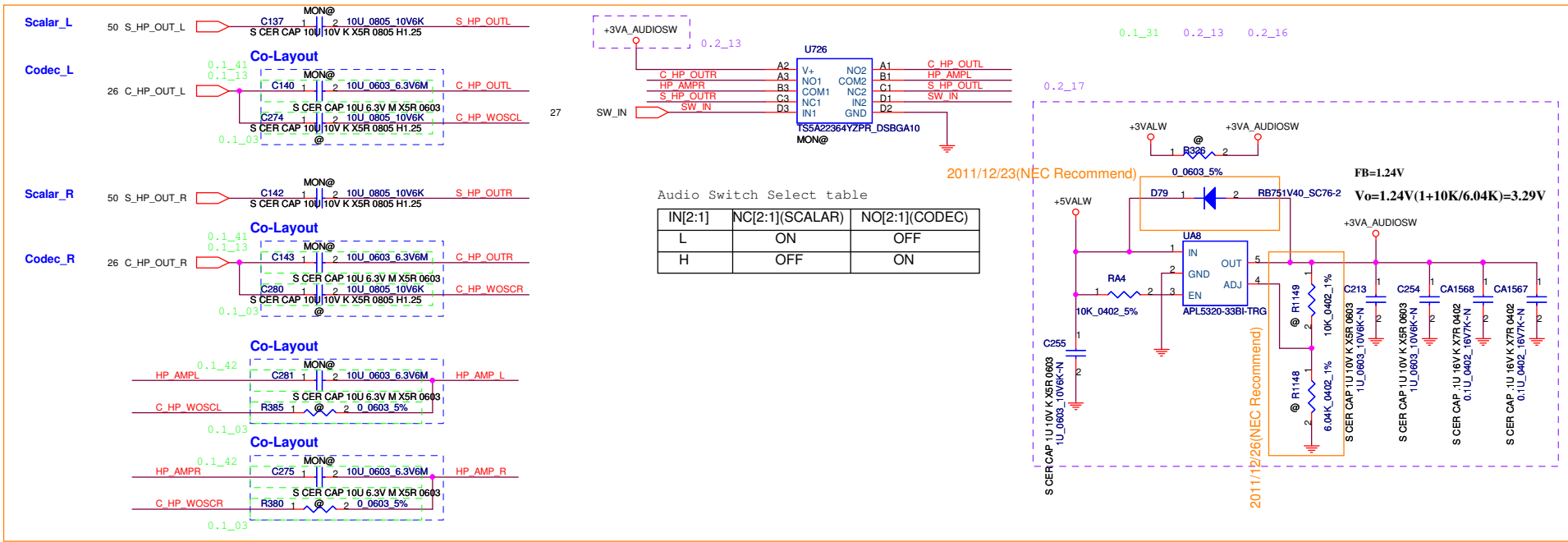


For HDMI/CODEC/MIC



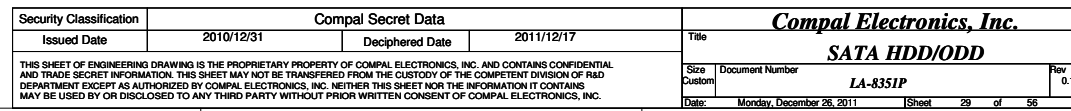
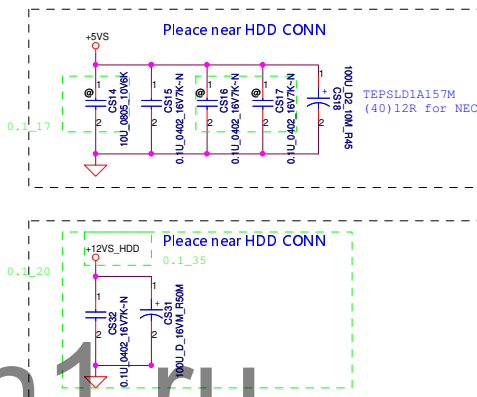
Update 2011/01/17



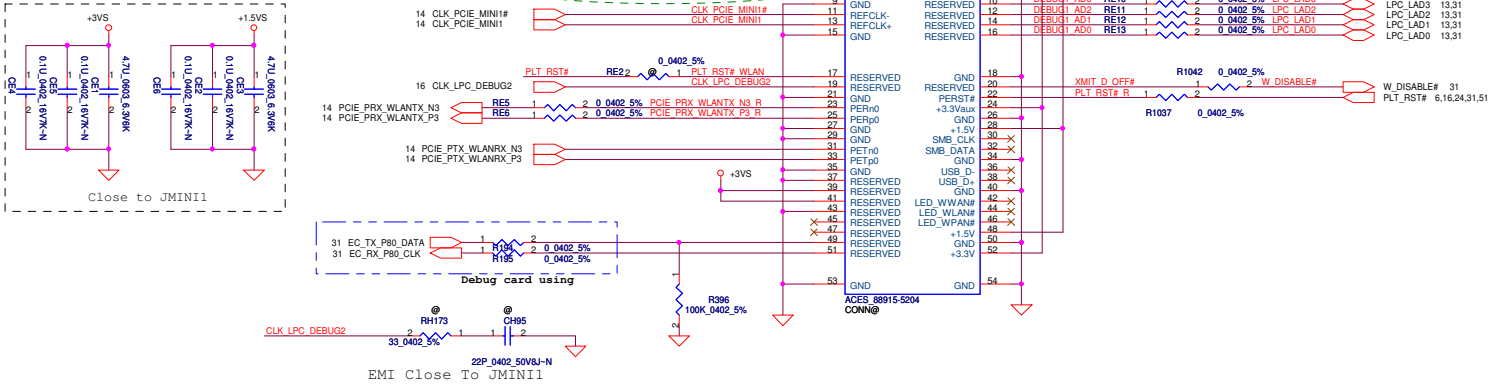


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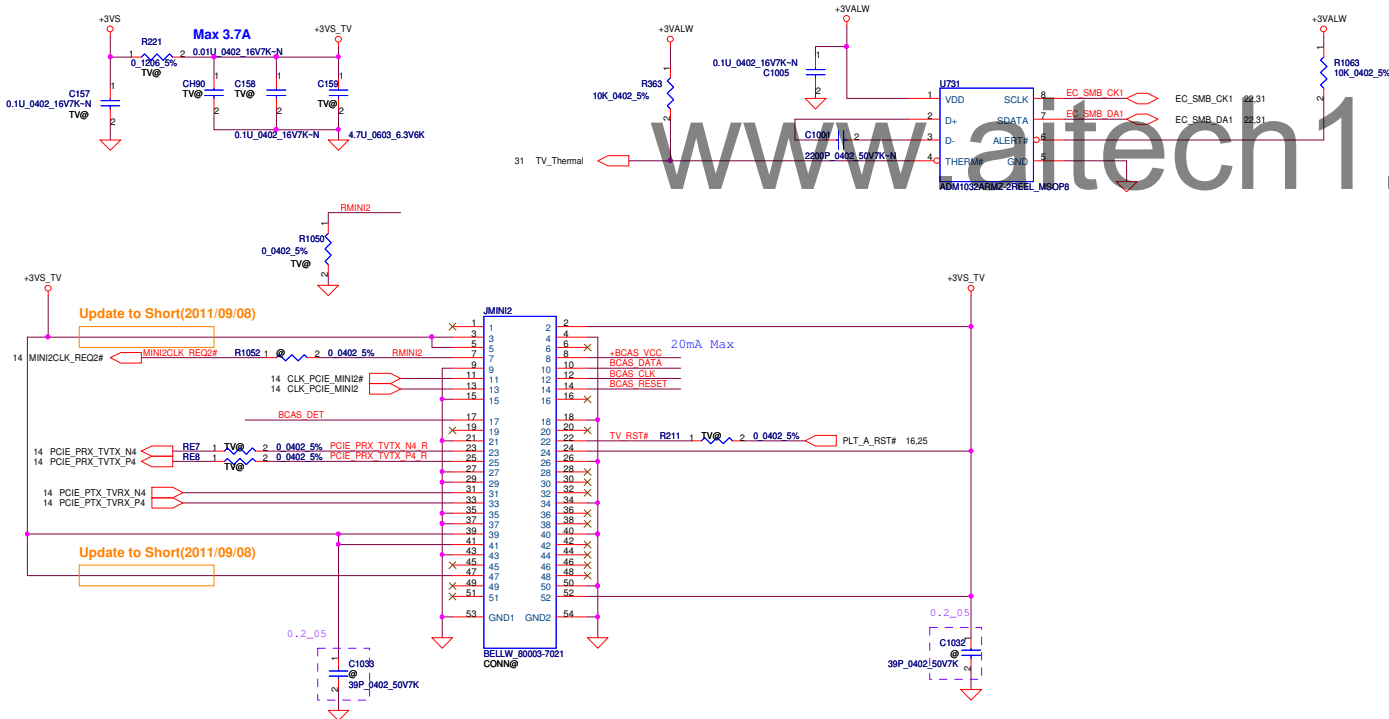
CS3 is X5R
CS4, CS5 are X7R



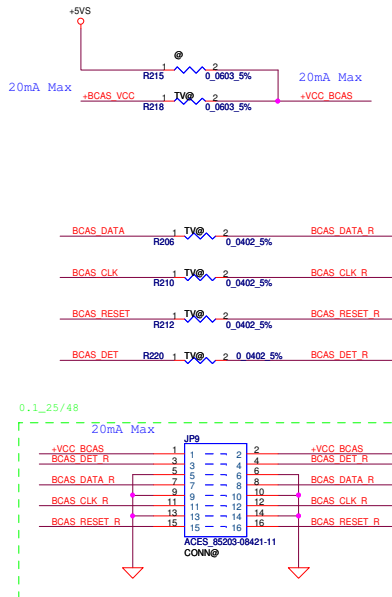
WLAN (Mini Card1)



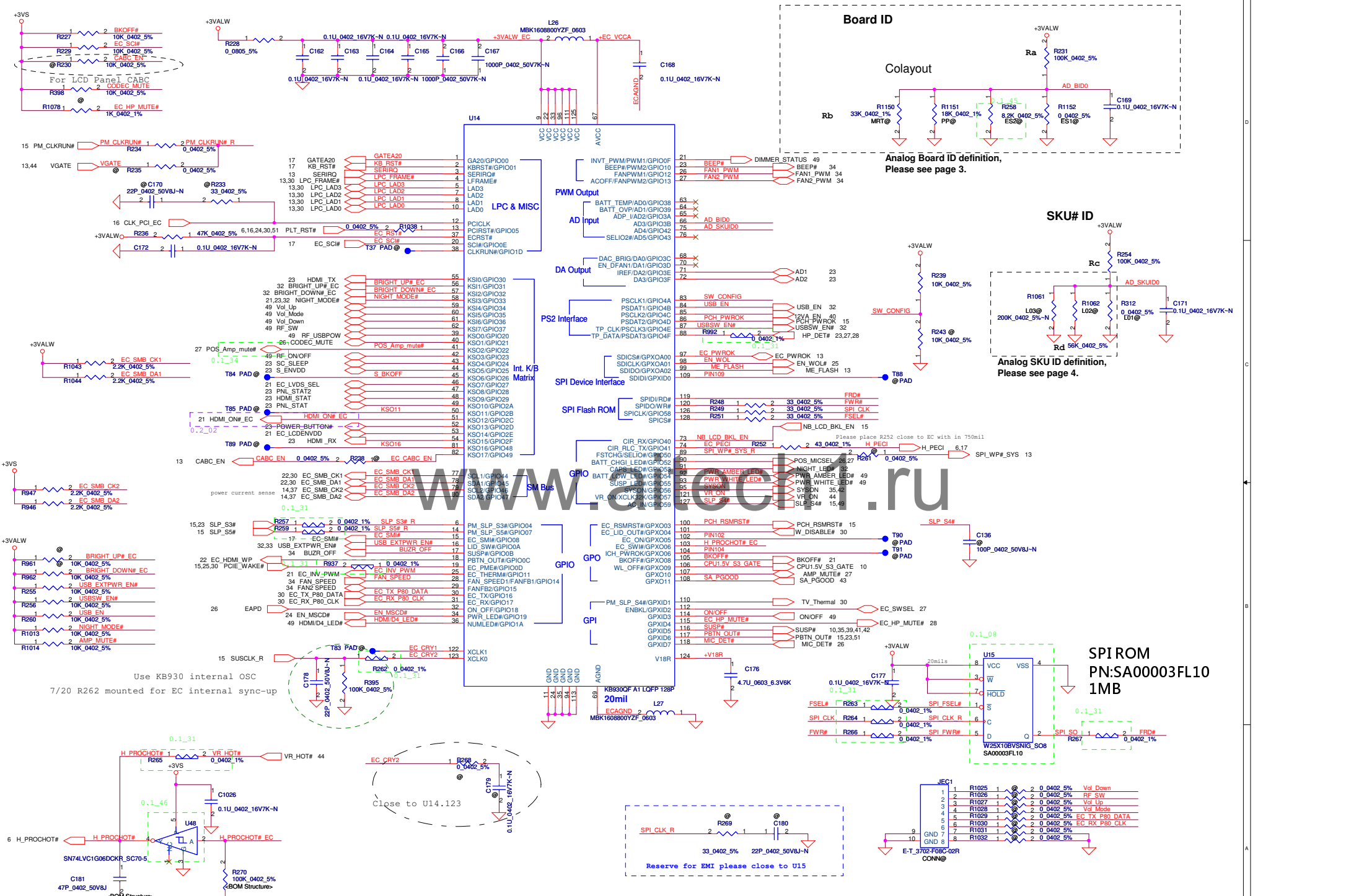
Mini Card Slot 2---TV tuner



B-CAS Circuit



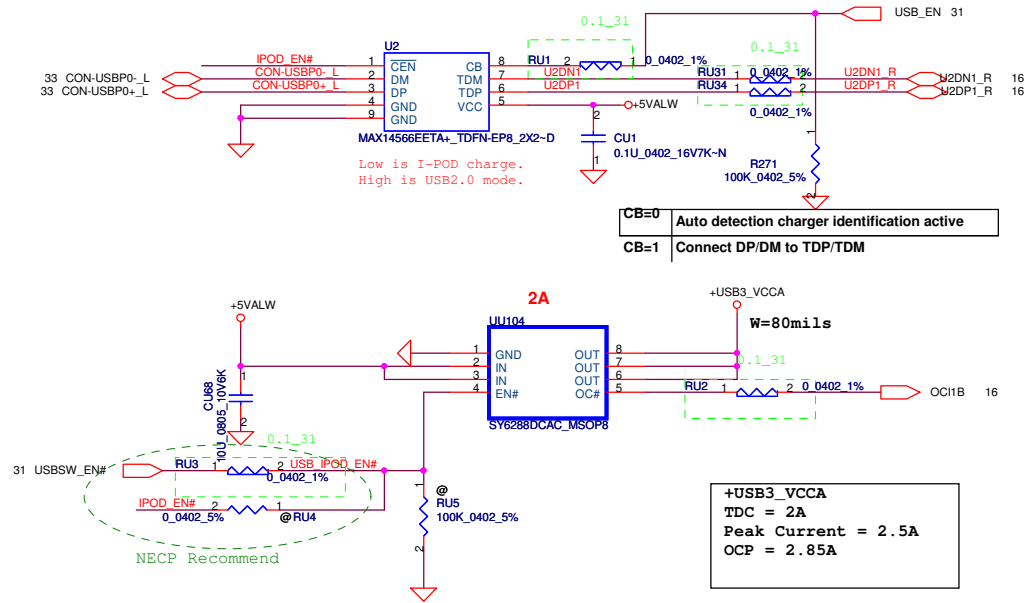
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| Security Classification | | Compal Secret Data | | Compal Electronics, Inc. PCIE WLAN/TV B-CAS | |
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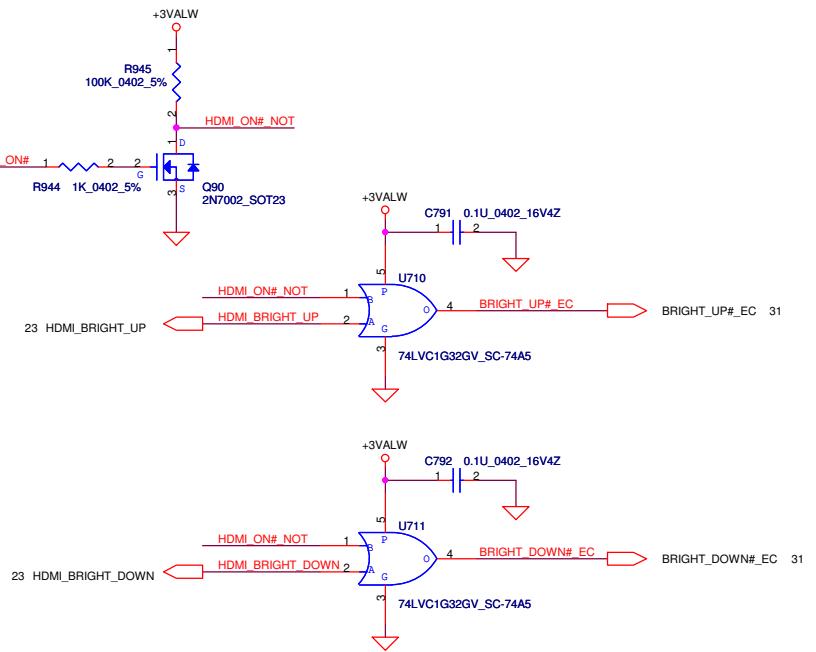
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| EC ENE-KB930 | | | | | |
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| Custom | | LA-835IP | | 0.1 | |
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IPOD USB PWR SW

GPIO pin
confirmed with EC

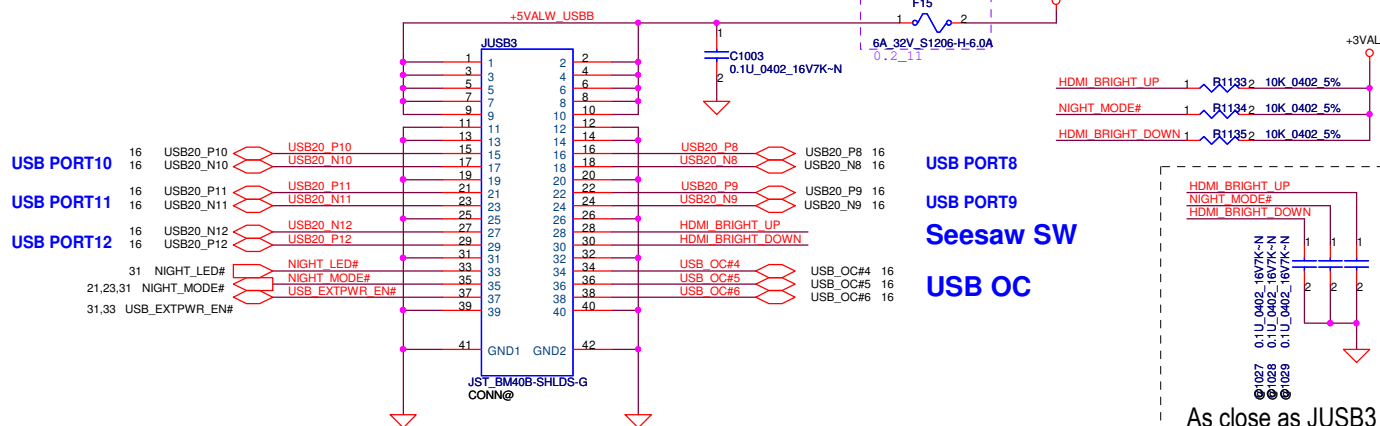


Co-lay
RU31 with RU48
RU34 with RU49
21,23,27 HDMI_ON#



USB Sub Board CONN.

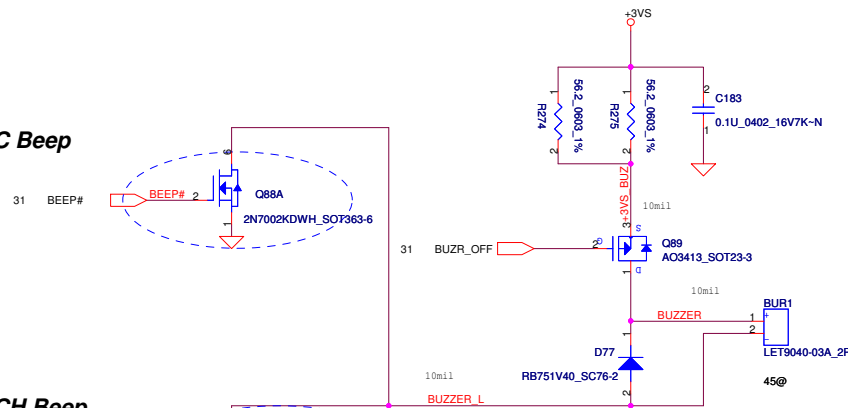
<Note>
Fuse Select (F15)
Rated current = Max current / 0.7
= 3.5A / 0.7 = 5 A



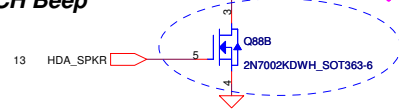
| Security Classification | | Compal Secret Data | | Compal Electronics, Inc. | |
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| | | | | Sheet 32 of 56 | |

BUZZER

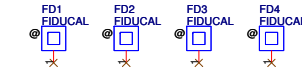
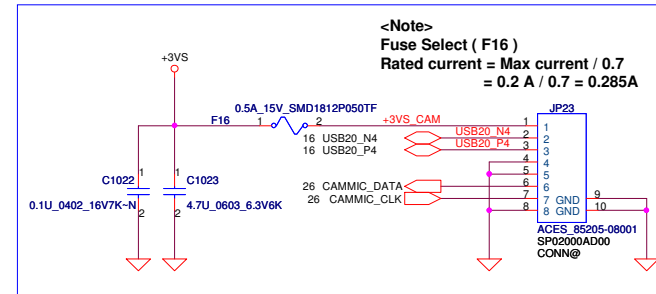
EC Beep



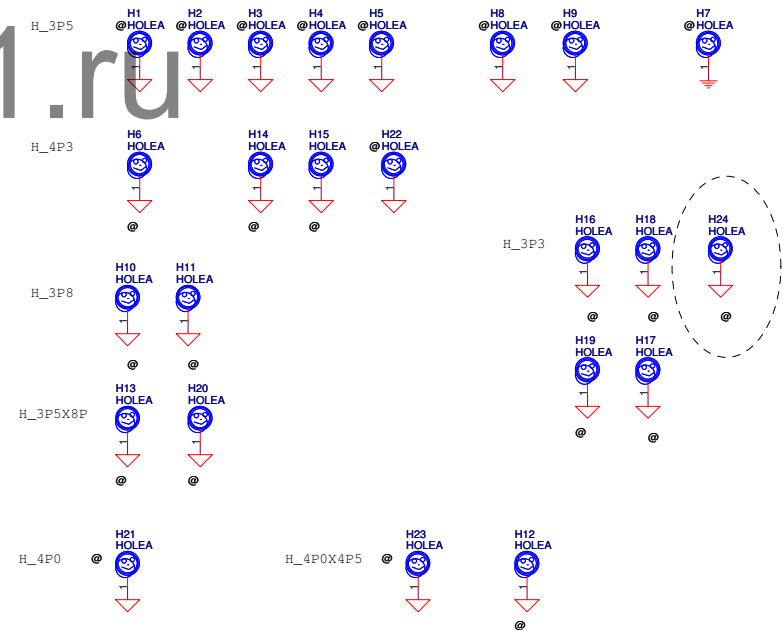
PCH Beep



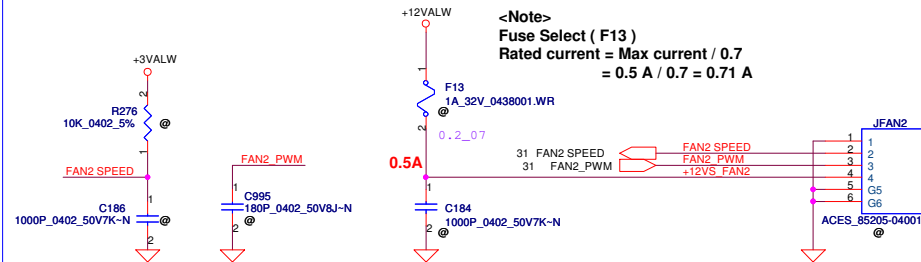
CAM



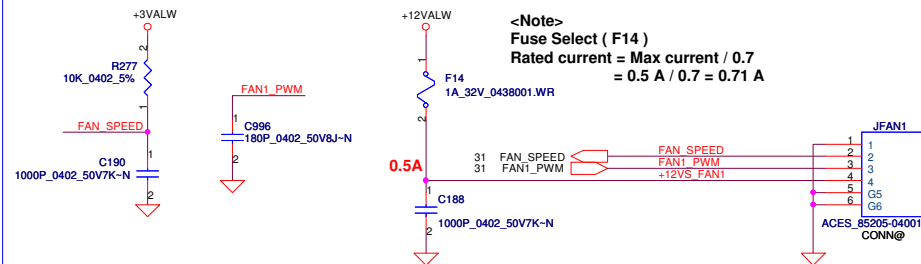
Screw



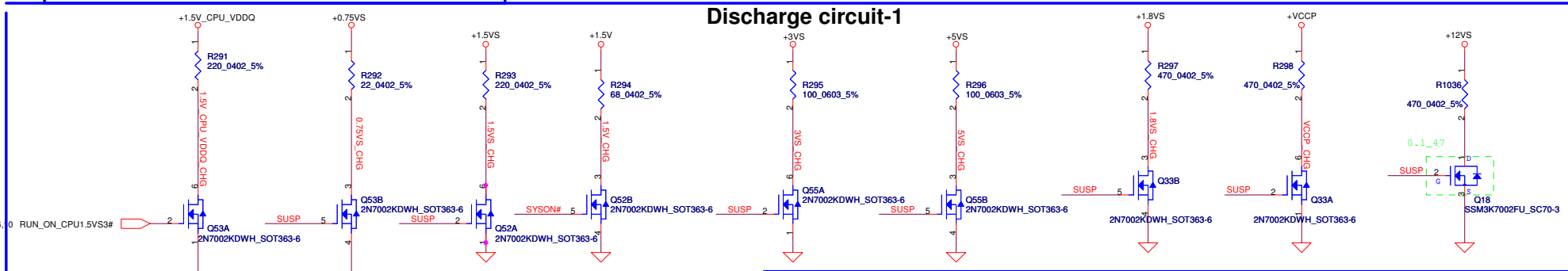
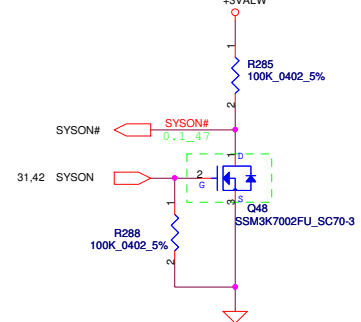
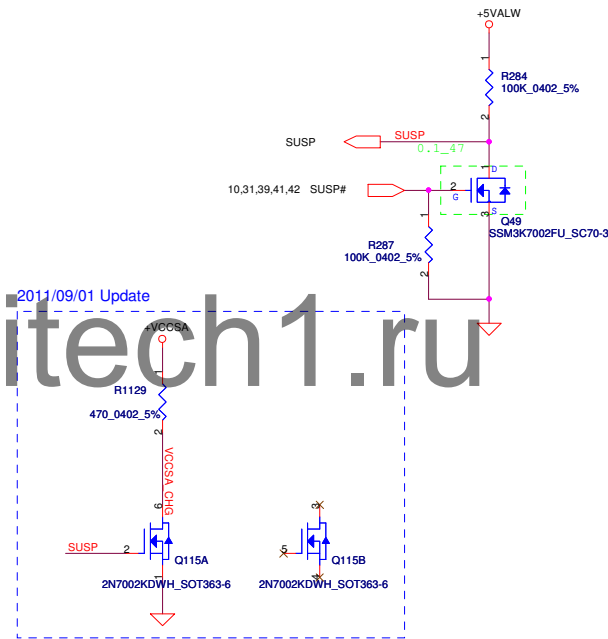
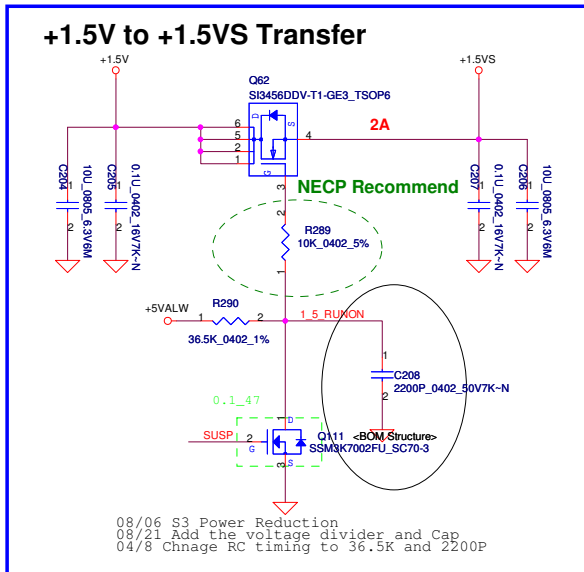
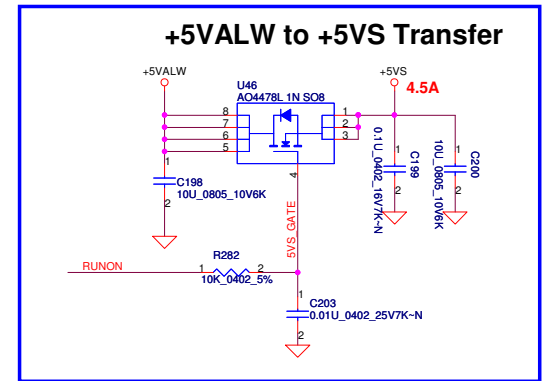
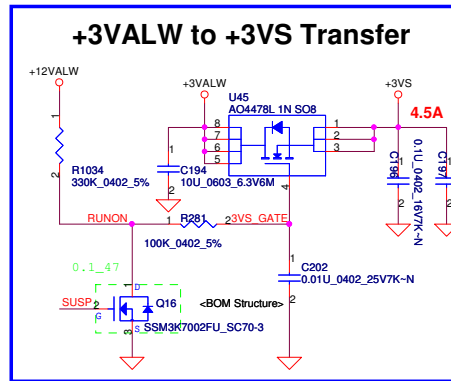
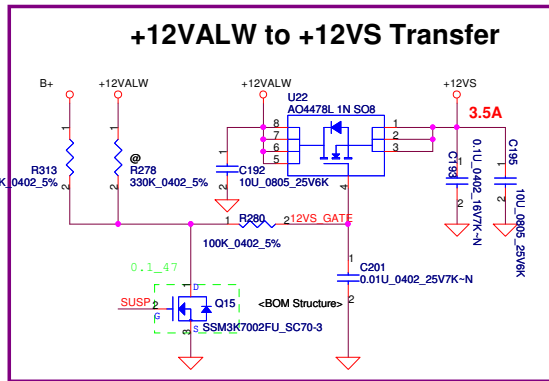
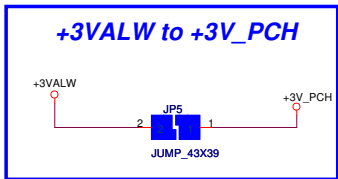
TV card Fan Control Circuit



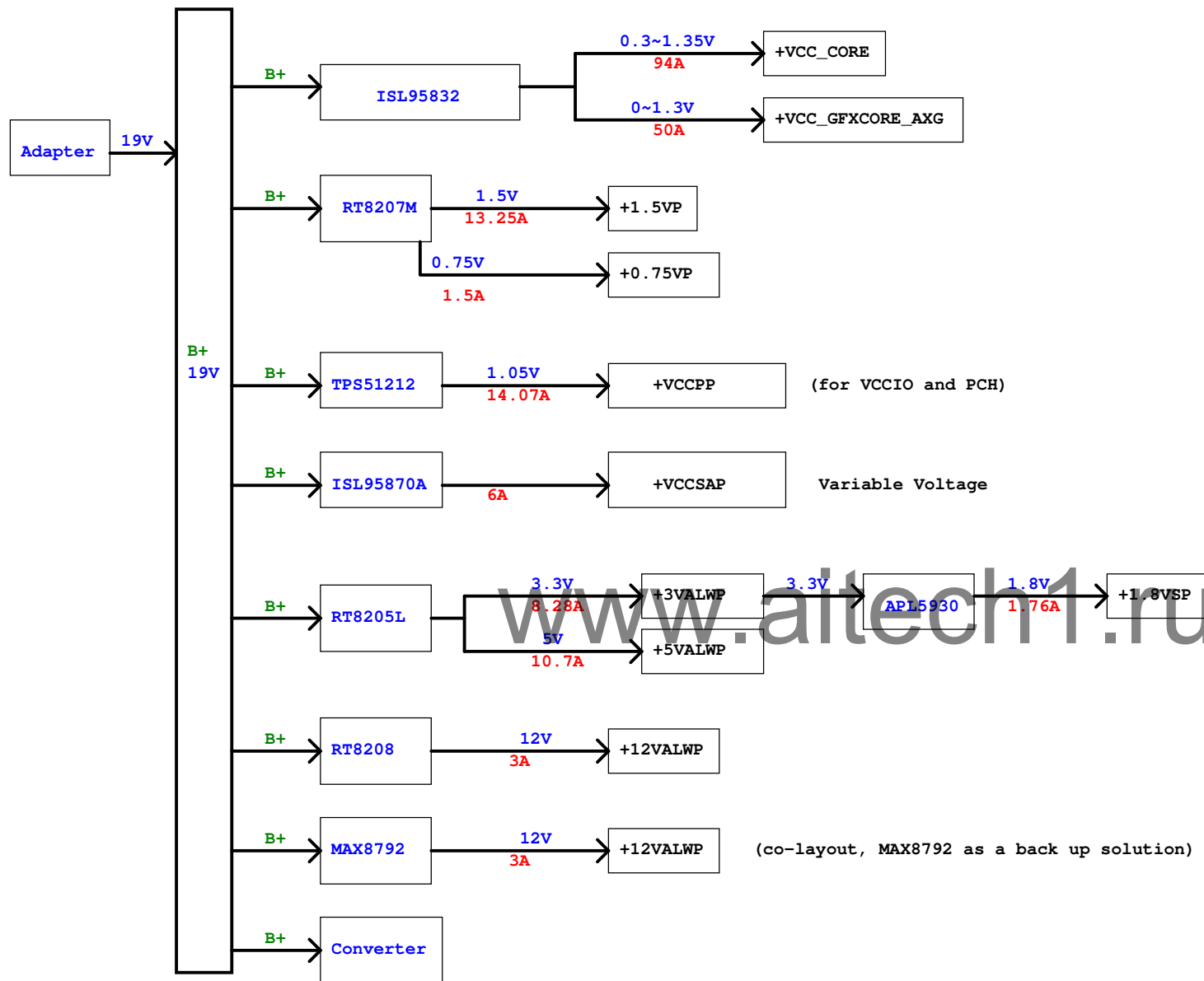
CPU Fan Control Circuit



| | | | | | | |
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| | | | | Date: | Monday, December 26, 2011 | Sheet 34 of 56 |



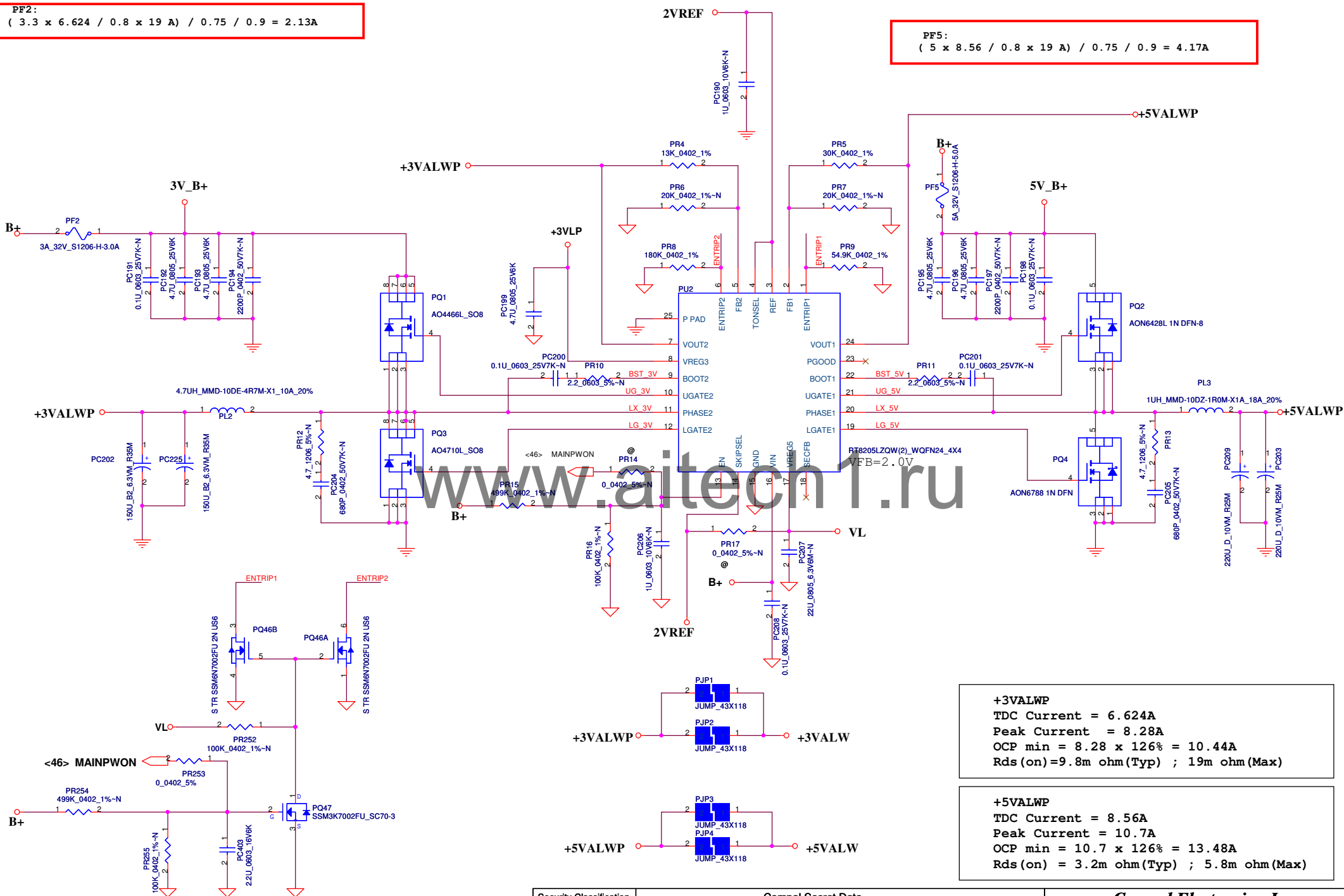
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| | | | | DC/DC Interface | | |
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| Size | Document Number | | | Rev |
| Date: Monday, December 26, 2011 | Sheet | 36 | of | 57 |
| | | | | 0.1 |

PF2:
 $(3.3 \times 6.624 / 0.8 \times 19 \text{ A}) / 0.75 / 0.9 = 2.13\text{A}$

PF5:
 $(5 \times 8.56 / 0.8 \times 19 \text{ A}) / 0.75 / 0.9 = 4.17\text{A}$

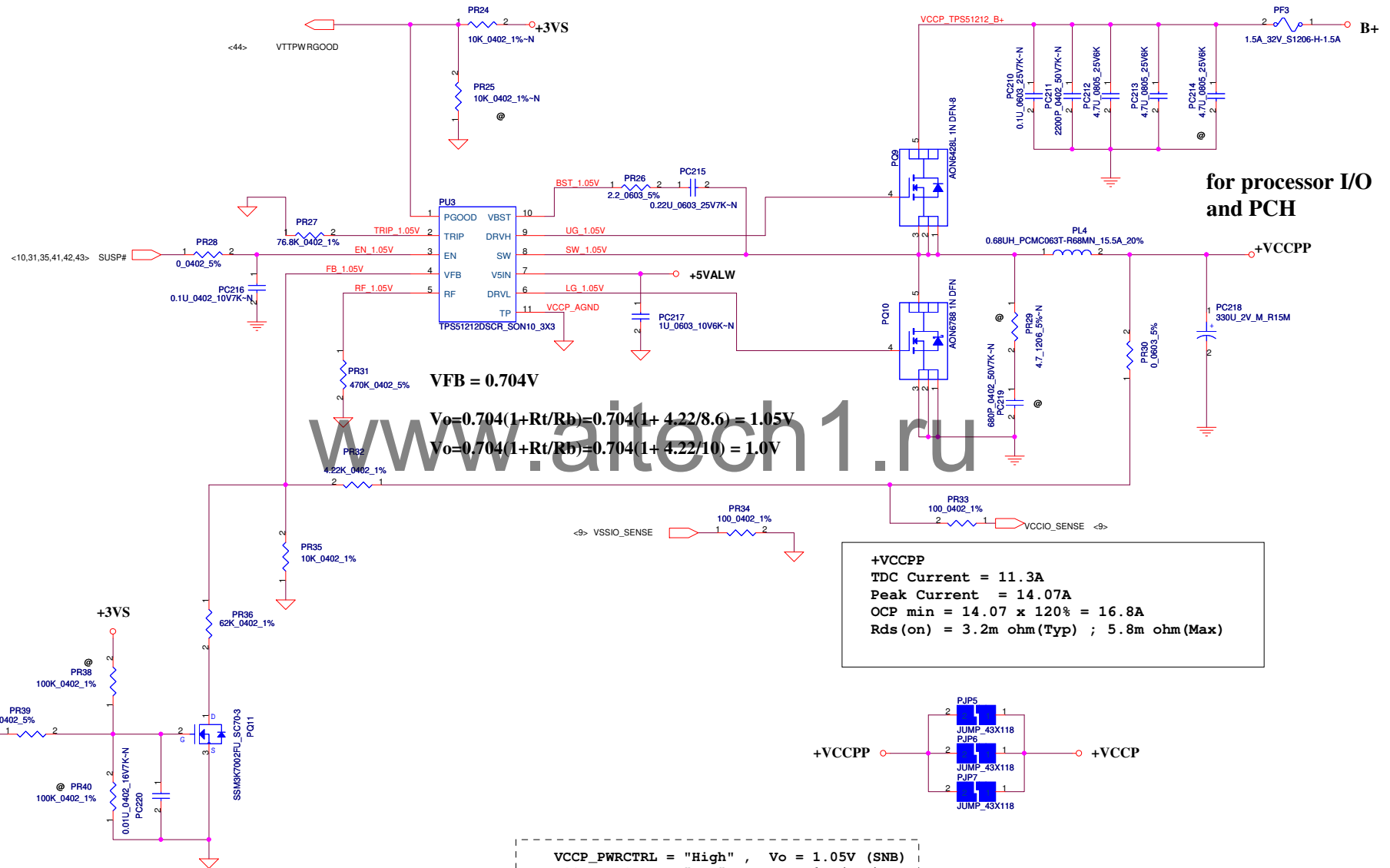


+3VALWP
TDC Current = 6.624A
Peak Current = 8.28A
OCP min = 8.28 x 126% = 10.44A
Rds(on) = 9.8m ohm(Typ) ; 19m ohm(Max)

+5VALWP
TDC Current = 8.56A
Peak Current = 10.7A
OCP min = 10.7 x 126% = 13.48A
Rds(on) = 3.2m ohm(Typ) ; 5.8m ohm(Max)

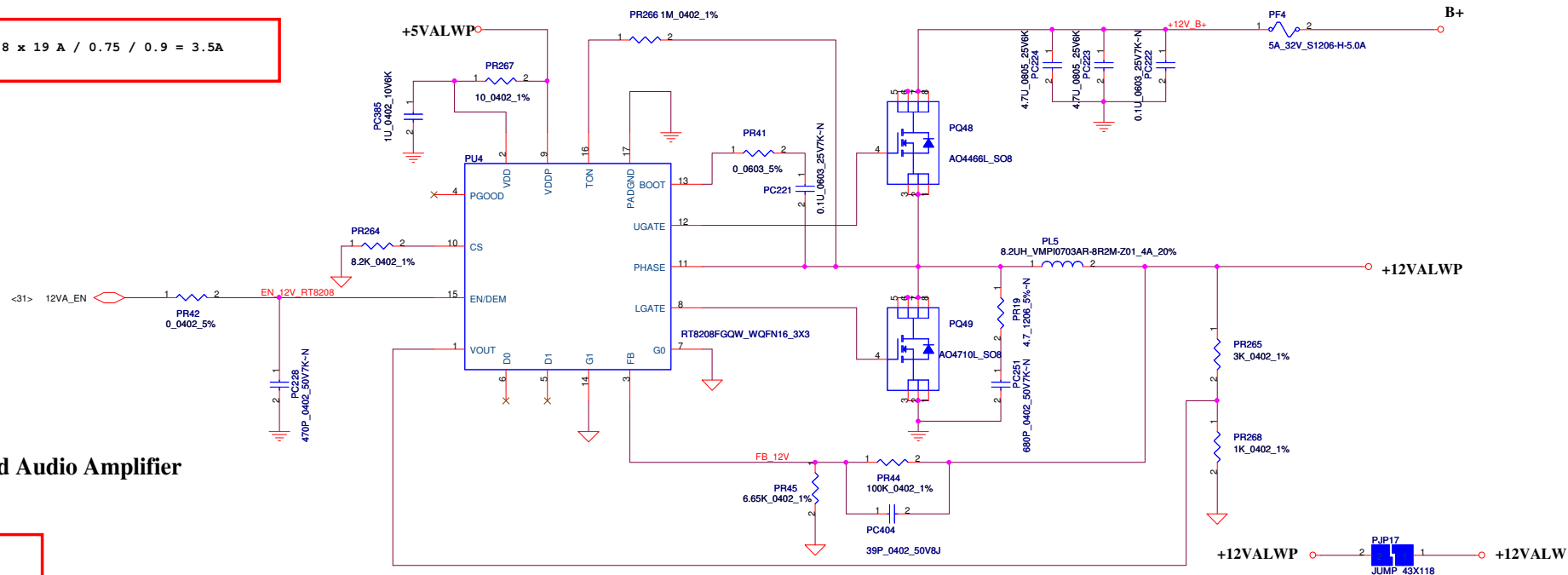
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| Size | Custom | Document Number | | Rev | 0.3 |
| Date: | Monday, December 26, 2011 | Sheet | 38 | of | 57 |

$$PF3: 1.05 \times 11.3 / 0.8 \times 19 \text{ A} / 0.75 / 0.9 = 1.15\text{A}$$



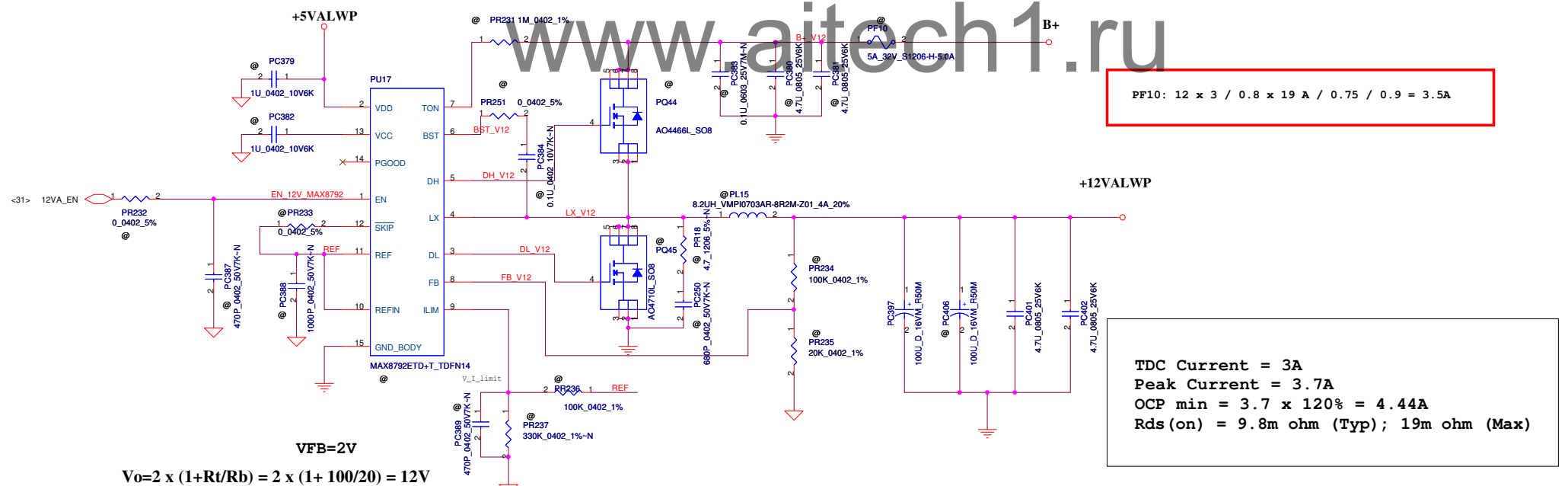
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| | | | | | | | | PWR+VCCPP | | | |
| | | | | | | | | Rev | | | |
| | | | | | | | | 0.3 | | | |
| | | | | | | | | Date: Monday, December 26, 2011 | | | |
| | | | | | | | | Sheet 39 of 57 | | | |

PF4: $12 \times 3 / 0.8 \times 19 \text{ A} / 0.75 / 0.9 = 3.5\text{A}$



for Fan, HDD and Audio Amplifier

CO-layout



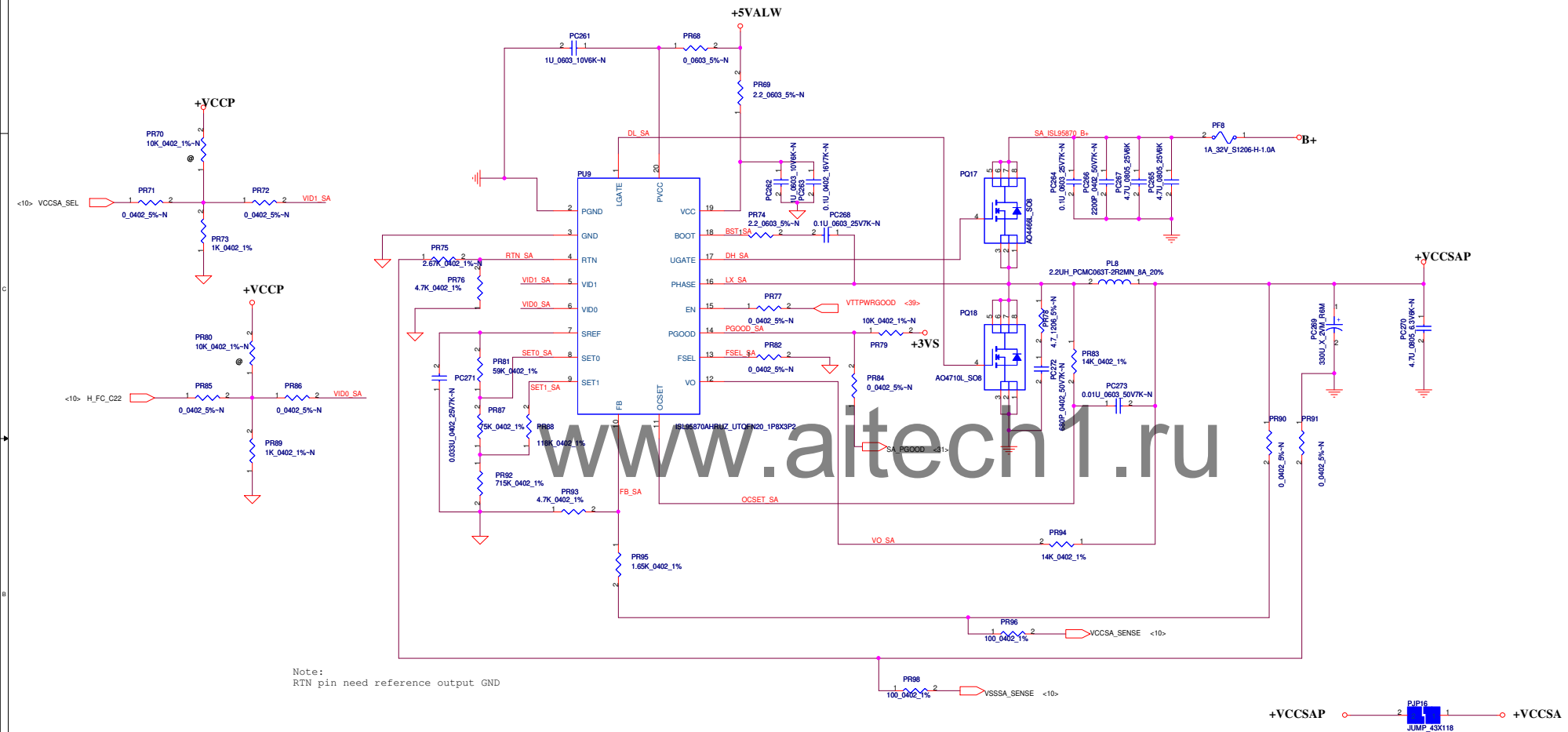
PF10: $12 \times 3 / 0.8 \times 19 \text{ A} / 0.75 / 0.9 = 3.5\text{A}$

TDC Current = 3A
Peak Current = 3.7A
OCP min = $3.7 \times 120\% = 4.44\text{A}$
Rds(on) = 9.8m ohm (Typ); 19m ohm (Max)

$V_{FB} = 2\text{V}$
 $V_o = 2 \times (1 + R_t/R_b) = 2 \times (1 + 100/20) = 12\text{V}$

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| | | | | Sheet | 40 of 57 |

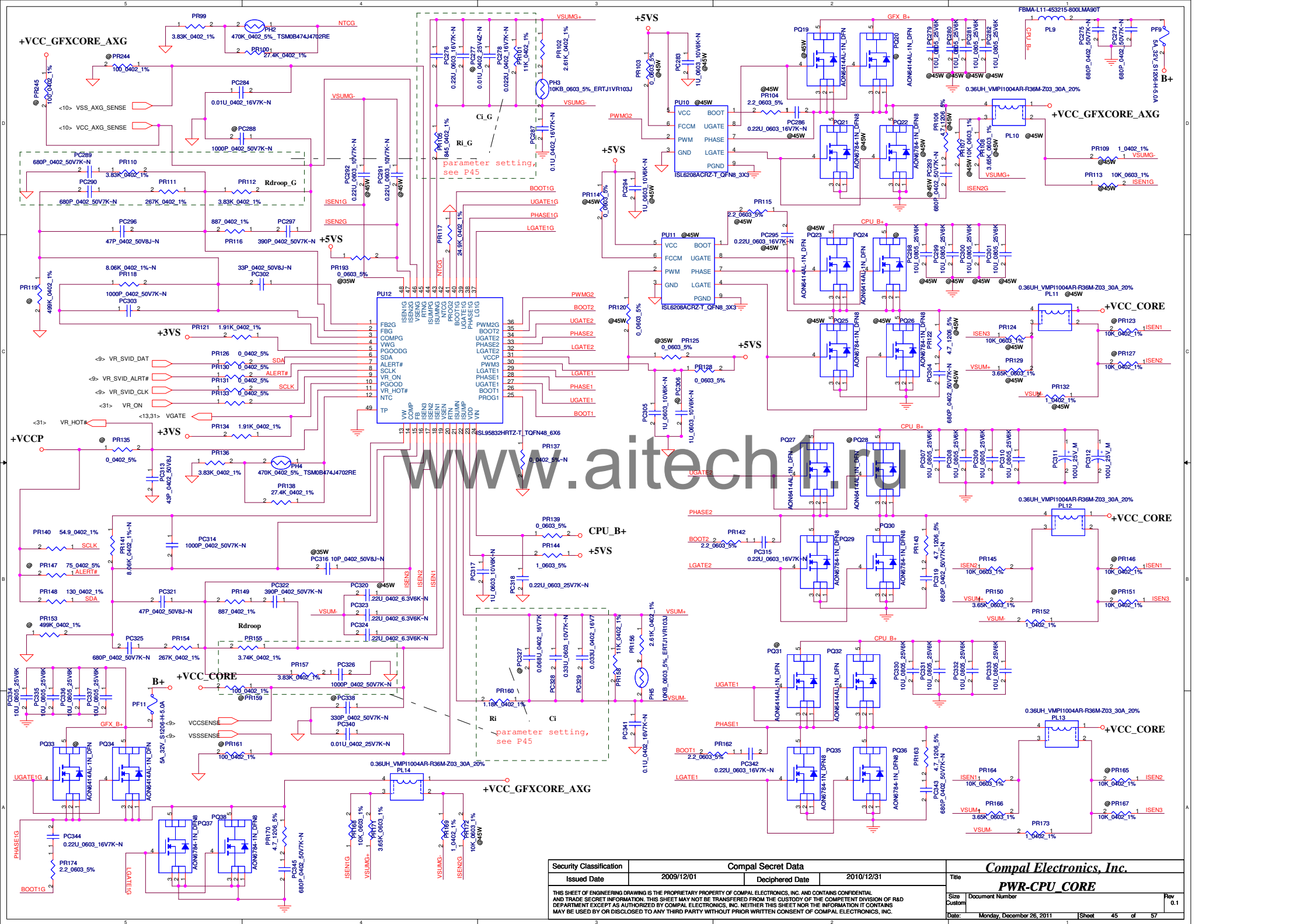
$$PF8: 0.9 \times 4.8 / 0.8 \times 19A / 0.75 / 0.9 = 0.42A$$



| VID[0] | VID[1] | VCCSA | Vout |
|---------|---------|--------|------|
| VID1_SA | VID0_SA | | |
| 0 | 0 | 0.9V | Yes |
| 0 | 1 | 0.8V | Yes |
| 1 | 0 | 0.725V | Yes |
| 1 | 1 | 0.675V | Yes |

+VCCSAP
TDC 4.8A
Peak Current 6A
OCP min = 6 x 120% = 7.2A
Rds(on)=9.8m ohm (Typ) 19.0m ohm(Max)

| | | | | |
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| Custom | | Document Number | | Rev | |
| Date | | Monday, December 26, 2011 | | 0.1 | |
| Sheet | | 45 | | of | |
| 57 | | | | | |

PF9:
We calculate the CPU core input current :
 $0.9 \times 54.6 / (0.8 \times 19 \text{ A}) / 0.75 / 0.9 = 4.78\text{A}$

PF11:
We calculate the GFX input current :
 $1.23 \times 35 / 0.8 \times 19 \text{ A} / 0.75 / 0.9 = 4.19\text{A}$

parameter setting table for different CPU

| | | | | | | | | | | | | | | |
|---------|-------------------|-------|--------|---------|------------------|-----------|--------|-----------------|--------|-------|---------|------------------|-----------|--------|
| | PR160 | PC327 | PC328 | PC329 | PR155 | PR157 | PC326 | PR105 | PC276 | PC277 | PC278 | PR112 | PR110 | PC289 |
| 45W CPU | 1.18K | @ | 0.33uF | 0.033uF | 3.74K | 3.83K | 1000pF | 845 | 0.22uF | @ | 0.022uF | 3.83K | 3.83K | 680pF |
| 35W CPU | 1.32K | @ | 0.22uF | 0.022uF | 3.09K | 3.83K | 1000pF | 1K | 0.1uF | @ | 0.033uF | 2.74K | 3.83K | 1000pF |
| purpose | VCORE OCP setting | | | | static load line | for D.VID | | GFX OCP setting | | | | static load line | for D.VID | |

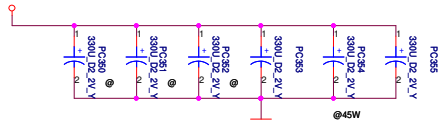
| 45W CPU | 35W CPU |
|--|--|
| CPU CORE peak current 94A TDC = 54.6A load line = 1.9m OHM OCP = 114A-141A VR setting CPU core OCP=115A | CPU CORE peak current 53A TDC = 32A load line = 1.9m OHM OCP = 63A-79A VR setting CPU core OCP=64A |
| Ri=PR160=1.18K-Ohm Ci=PC327//PC328//PC329=0.363uF both for CPU CORE OCP setting Rdroop=PR155=3.74K-Ohm for load line PC326=1000pF, PR157=3.83K-Ohm for D.VID | Ri=PR160=1.33K-Ohm Ci=PC327//PC328//PC329=0.242uF both for CPU CORE OCP setting Rdroop=PR155=3.09K-Ohm for load line PC326=1000pF, PR157=3.83K-Ohm for D.VID |
| GFX peak current 50A TDC = 35A load line = 3.9m OHM OCP = 60A-75A VR setting GFX core OCP=60A | GFX peak current 33A TDC = 21.5A load line = 3.9m OHM OCP = 40A-50A VR setting GFX core OCP=40A |
| Ri_G=PR105=845-Ohm Ci_G=PC276//PC277//PC278=0.242uF both for GFX OCP setting Rdroop_G=PR112=3.83K-Ohm for load line PC289=680pF, PR110=3.83K-Ohm for D.VID | Ri_G=PR105=1K-Ohm Ci_G=PC276//PC277//PC278=0.133uF both for GFX OCP setting Rdroop_G=PR112=2.74K-Ohm for load line PC289=1000pF, PR110=3.83K-Ohm for D.VID |

control table for 45W and 35W CPU

| | |
|------------------------|------------|
| not mount | ● |
| mount for 35W CPU only | ●35W |
| mount for 45W CPU only | ●45W |
| mount for 35W 45W both | no marking |

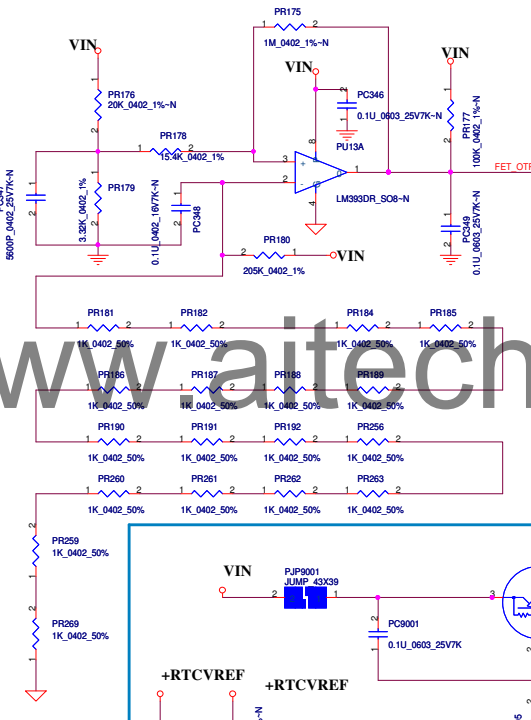
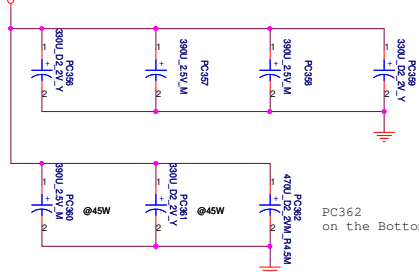
Output Capacitor for GFX:

+VCC_GFXCORE_AXG



Output Capacitor for CPU Core :

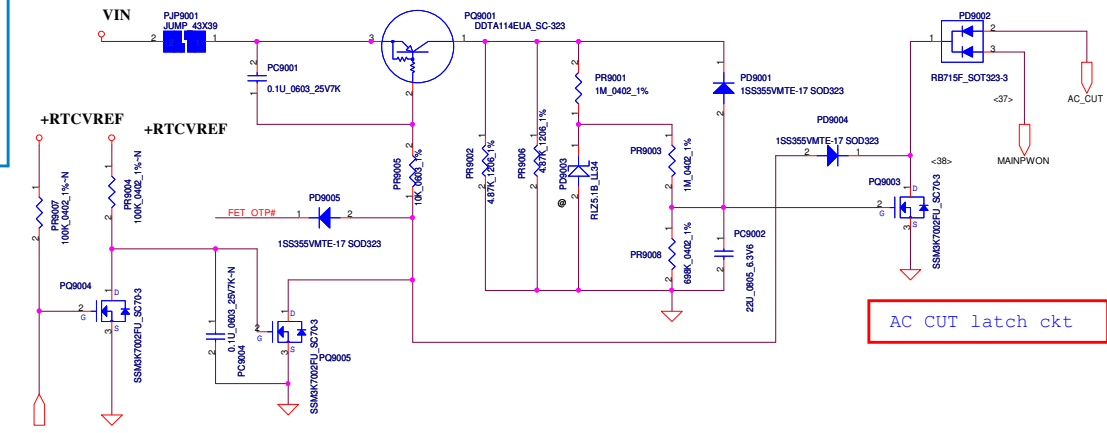
+VCC_CORE



Posestos

PR181, PR182, PR184, PR185, PR186, PR187, PR188, PR189, PR190, PR191, PR192, PR256, PR259, PR260, PR261, PR262, PR263, PR269 are PTC

PR181 close to PQ3
PR182 close to PQ2
PR184 close to PQ10
PR185 close to PQ17
PR186 close to PQ25
PR187 close to PQ35
PR188 close to PQ30
PR189 close to PQ38
PR190 close to PQ21
PR191 close to PQ15
PR192 close to PQ45
PR256 close to PQ39
PR259 close to PQ31
PR260 close to PQ27
PR261 close to PQ33
PR262 close to PQ24
PR263 close to PQ19
PR269 close to PQ48



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|---|------------|--------------------|------------|-------|---------------------------|
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| | | | | Sheet | 46 of 57 |
| | | | | Rev | 0.3 |

Compal Electronics, Inc.

Version change list (P.I.R. List)

ES1 to ES2 for PWR

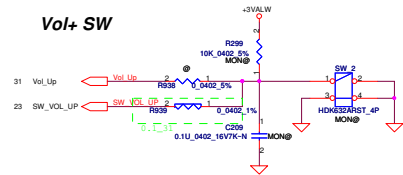
| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
|------|-------------|--|------|-----|---|-------------|-------|
| 1 | | fine tune the OCP of +VCCSAP | | 43 | change the PR83 and PR94 from 19.1K_0402_1% to 14K_0402_1% | 27-Oct 2011 | |
| 2 | | | | | | | |
| 3 | | | | | change the PR81 from 16.5K_0402_1% to 59K_0402_1% | | |
| 4 | | | | | | | |
| 5 | | set the Vout of +VCCSAP for IVB | | 43 | change the PR87 from 0_0402_1% to 75K_0402_1% | 27-Oct 2011 | |
| 6 | | | | | change the PR88 from 130K_0402_1% to 118K_0402_1% | | |
| 7 | | | | | | | |
| 8 | | | | | change the PR95 from 2.67K_0402_1% to 1.65K_0402_1% | | |
| 9 | | | | | | | |
| 10 | | enlarge the capacitance for CPU core out put capacitor | | 45 | change the PC357 and PC358 and PC360 from 330U_D2_2V_Y to 390U_2.5V_M | 27-Oct 2011 | |
| 11 | | | | | | | |
| 12 | | enlarge the capacitance for GFX out put capacitor | | 45 | change the PC350, PC351, PC352 and PC353 from 330U_D2_2V_Y to 390U_2.5V_M | 27-Oct 2011 | |
| 13 | | | | | | | |
| 14 | | | | | | | |
| 15 | | enlarge the capacitance for +12VALWP output capacitor | | 40 | delete PC406, change the PC397 from 100U_D_16VM_R50M to 220U_16V_M | 27-Oct 2011 | |
| 16 | | | | | | | |
| 17 | | change the GFX output capacitor for layout placement | | 45 | change the PC350, PC351, PC352 and PC353 from 390U_2.5V_M to 330U_D2_2V_Y | 7-Nov 2011 | |
| 18 | | | | | | | |
| 19 | | change the latch ckt delay time resistor | | 45 | change the PR9003 from 619K_0603_1% to 511K_0402_1% | 8-Nov 2011 | |
| 20 | | | | | | | |
| 21 | | *****end***** | | | | | |
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|---|------------|--------------------|------------|--------------------------|---------------------------|
| Security Classification | | Compal Secret Data | | Title | |
| Issued Date | 2010/12/17 | Deciphered Date | 2011/12/17 | Compal Electronics, Inc. | |
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| | | | | Custom | LA-8351P |
| | | | | Date | Monday, December 26, 2011 |
| | | | | Sheet | 47 of 57 |
| | | | | Rev | 1.0 |

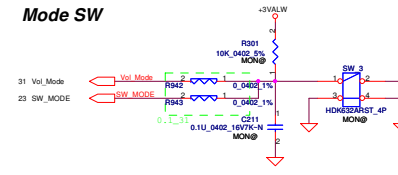
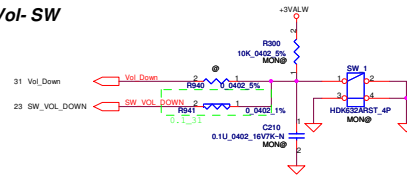
Version change list (P.I.R. List)

ES2 to PP for PWR

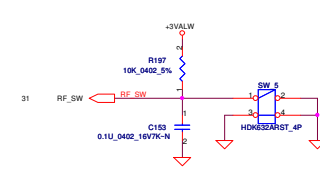
| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
|------|---|---|------|-----|--|------|-------|
| 1 | | | | 44 | 1. 45W CPU PR160 changed to be 1.18K-OHM from 1.15K-OHM PC329 populated (original is 0), PR155 changed to be 3.74K-OHM from 3.57K-OHM PR112 changed to be 3.83K-OHM from 3.92K-OHM PC289 changed to be 680pF from 1000pF. | | |
| 2 | | fine tune the OCP and load line setting for 35W and 45W CPU | | | | | |
| 3 | | | | | | | |
| 4 | | | | | | | |
| 5 | | | | | | | |
| 6 | | | | | 2. 35W CPU PR155 changed to be 3.09K-OHM from 3.01K-OHM PR105 changed to be 1K-OHM from 909-OHM PR112 changed to be 2.74K-OHM from 2.61K-OHM. | | |
| 7 | | | | | | | |
| 8 | | | | | | | |
| 9 | | | | | | | |
| 10 | | +12VALWP output capacitor design changed back to as ES1 stage, from ES2 Aluminum electrolytic type back to ES1 Polymer type. | | 40 | changed PC397 to be 100U_D_16VM_R50M from 220U_16V_M create PC406 to be 100U_D_16VM_R50M as ES1. | | |
| 11 | NEC EE's audio test was fail. it is relative to +12VALW. | | | | | | |
| 12 | | | | | | | |
| 13 | | | | 45 | 1. 45W CPU VCORE: PC356,PC359,PC361 changed to be 330U_D2_2V_Y from 470U_D2_2VM_R4.5M GFX CORE: PC354,PC355 changed to be 330U_D2_2V_Y from 470U_D2_2VM_R4.5M, PC352 changed to be 0 | | |
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| 18 | | reduce the CPU VCORE and GFX CORE output capacitor | | | 2. 35W CPU VCORE: PC356,PC359 changed to be 330U_D2_2V_Y from 470U_D2_2VM_R4.5M GFX CORE: PC355 changed to be 330U_D2_2V_Y from 470U_D2_2VM_R4.5M PC352 and PC354 changed to be 0 | | |
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| 24 | VCCSA PGOOD abnormal signal | VCCSA PGOOD works abnormally | | 43 | connected the IC's pin #19 and #20 to +5VALW from +5VS | | |
| 25 | | *****end***** | | | | | |
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Vol+ SW

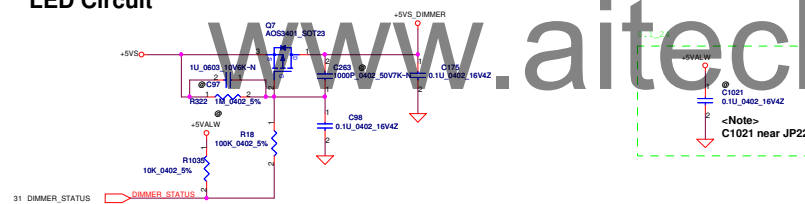
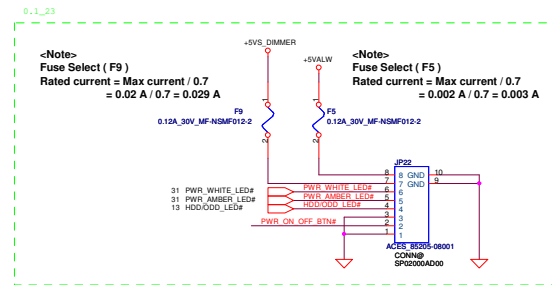
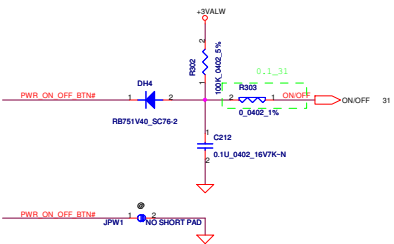
Mode SW



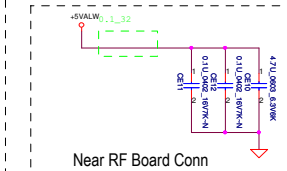
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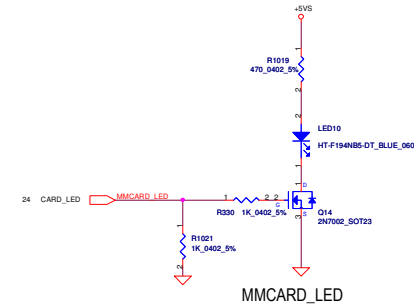
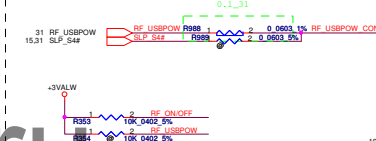
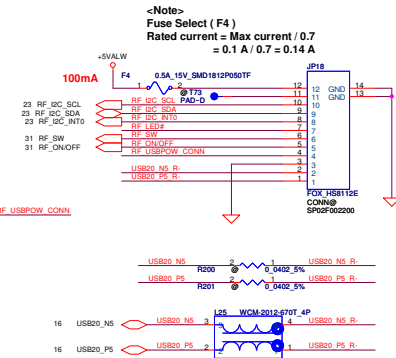
LED Circuit



Near RF Board Conn



<Note>
Fuse Select (F4)
Rated current = Max current / 0.7
= 0.1 A / 0.7 = 0.14 A



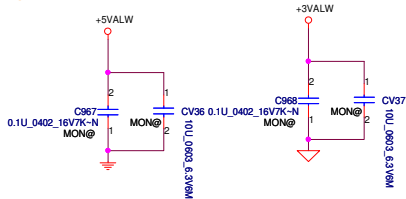
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| Issued Date | | Deciphered Date | | Size | |
| 2010/12/31 | | 2011/12/17 | | Document Number | |
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| | | | | LA-835P | |
| Date: | | | | Monday, December 26, 2011 | |
| | | | | Sheet 49 of 56 | |

| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
|------|-------------|-------------------|------|-----|-------------|------|-------|
| 1 | | *****end***** | | | | | |
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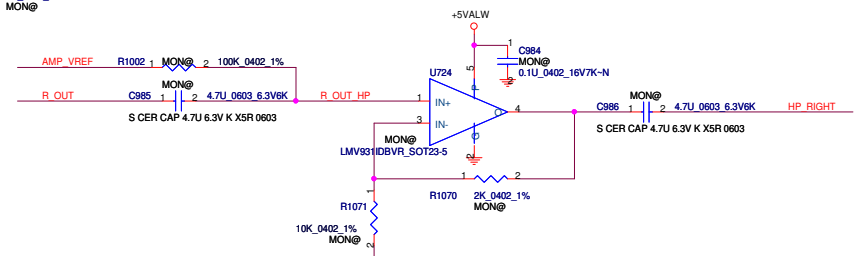
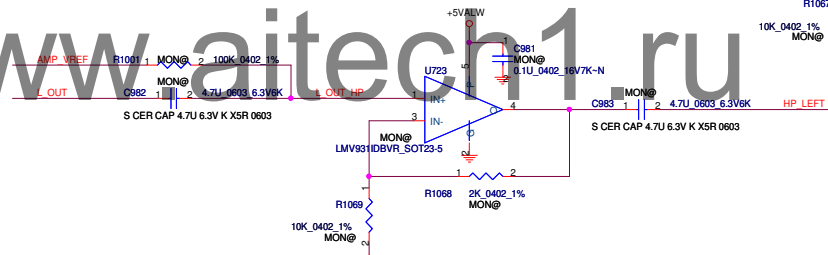
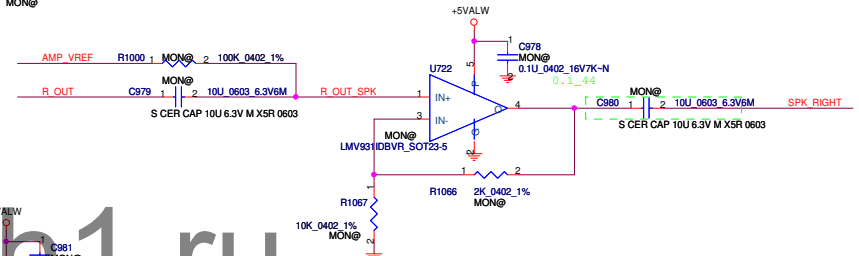
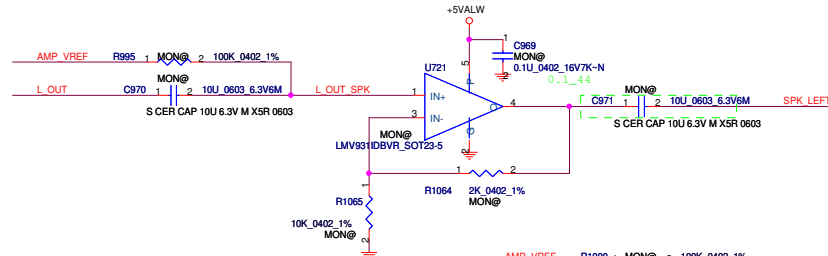
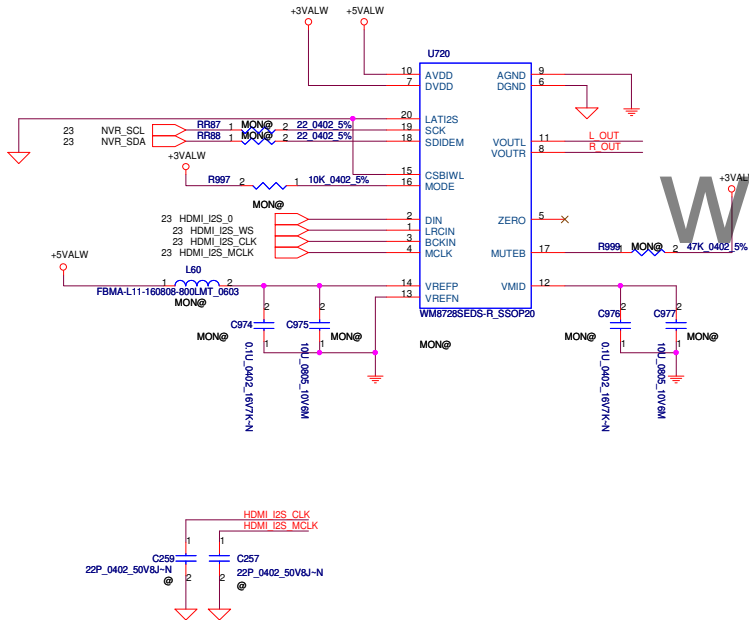
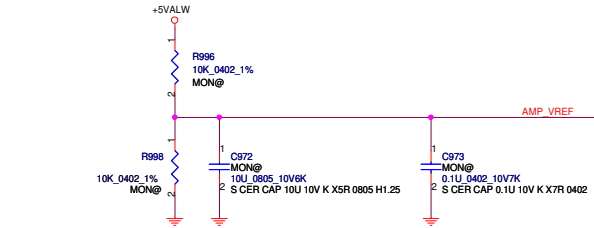
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| | | | | Size | Document Number | Rev |
| | | | | Custom | LA-8351P | 1.0 |
| Date: Monday, December 26, 2011 | | | | Sheet | 45 of 57 | |

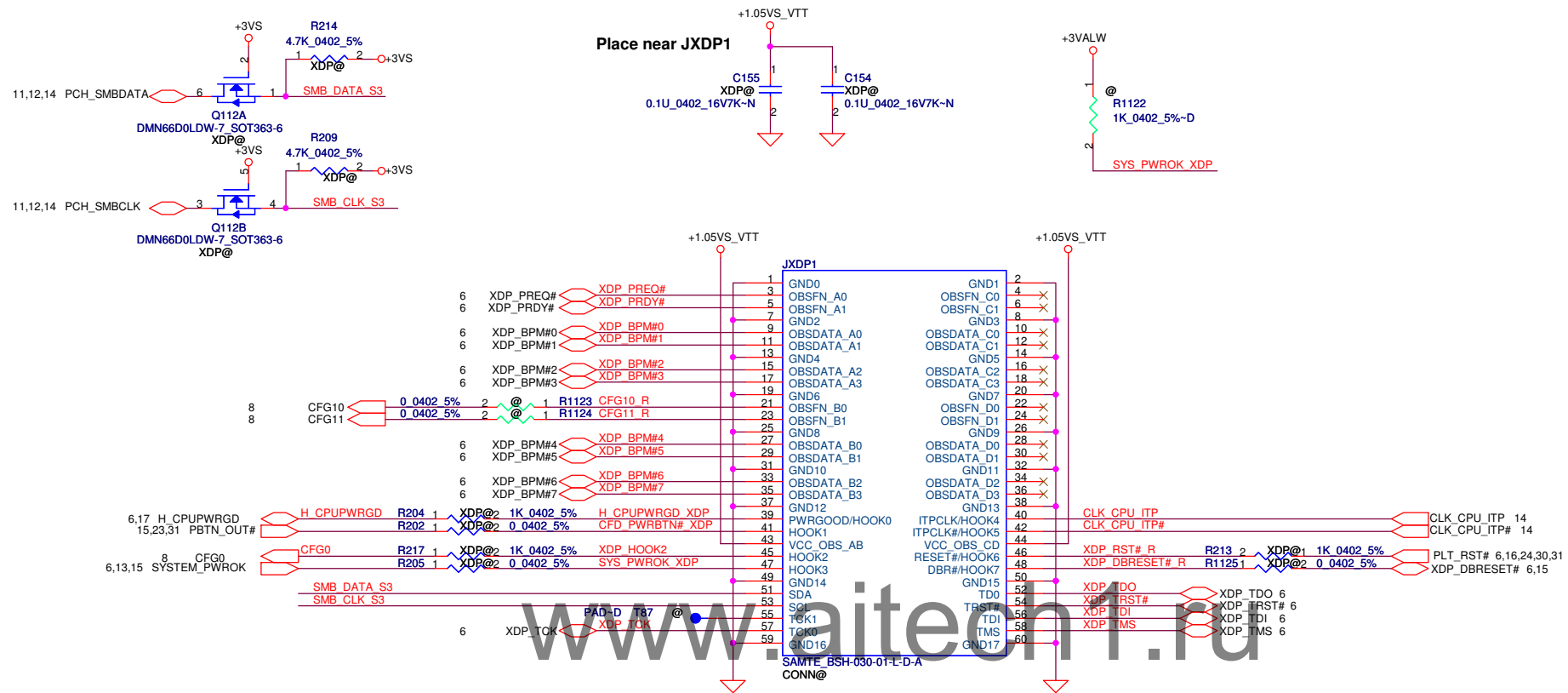
Update 2011/02/10



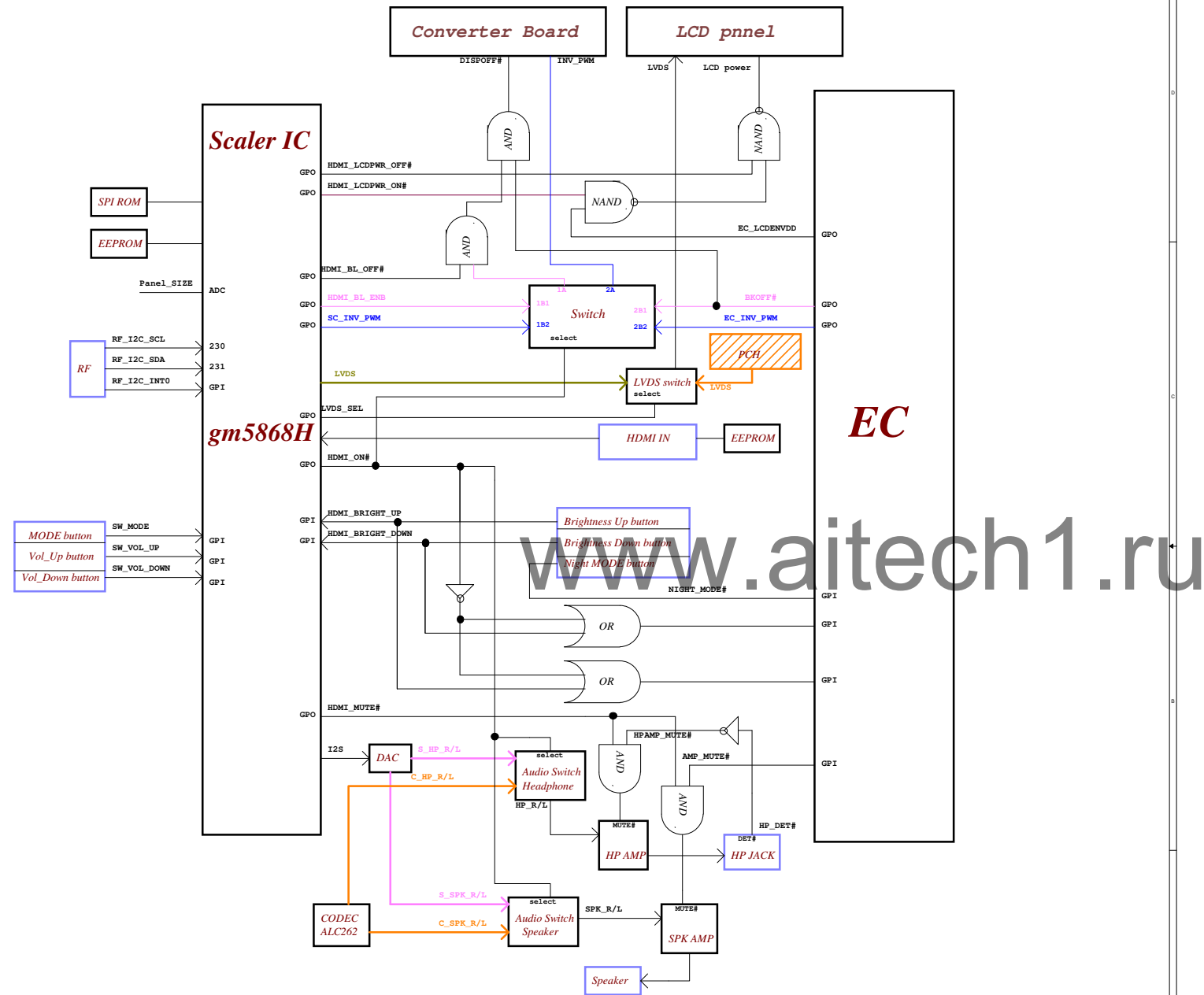
ALL Close to Audio Area



| Security Classification | | Compal Secret Data | | Compal Electronics, Inc. | |
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| Issued Date | 2010/12/31 | Deciphered Date | 2011/11/01 | Title | DAC_WM8728SEDS |
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| | | | | LA-8351P | Rev 0.1 |
| | | | | Date | Monday, December 26, 2011 |
| | | | | Sheet | 50 of 56 |



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| Size | | Document Number | | Rev | |
| B | | LA-8351P | | 0.1 | |
| Date: | | Monday, December 26, 2011 | | Sheet 51 of 56 | |



Note Color

Version change list (P.I.R. List)

ES1 to ES2 for HW

| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
|------|-------------|-------------------|------|----------|---|-------|-------|
| 1 | | HW Design | 0.1 | PG#26 | Add pull high 100Kohm resistor R1132 on MIC_DET# | 10/17 | ES2 |
| 2 | | HW Design | 0.1 | PG#27 | Change R983/R984/R1092/R1094 BS from PCH8 to 8 | 10/17 | ES2 |
| 3 | | HW Design | 0.1 | PG#28 | Change C274/C280/R1077/R380/R385 BS from PCH8 to 8 | 10/17 | ES2 |
| 4 | | HW Design | 0.1 | PG#26 | Change R1080/C1007/C1008/U735/R1105/R1106/R1130 BS to POS8 | 10/17 | ES2 |
| 5 | | HW Design | 0.1 | PG#26 | Change R1107/R1108 BS to NPOS8 | 10/17 | ES2 |
| 6 | | HW Design | 0.1 | PG#27 | Change CA1576/U737/Power Off Speaker & MIC-IN OP-AMP BS to POS8 | 10/17 | ES2 |
| 7 | | HW Design | 0.1 | PG#27 | Change R1109/R1110/R1131 BS to NPOS8 | 10/17 | ES2 |
| 8 | | HW Design | 0.1 | PG#31 | Change U15 from SA00002C100 to SA00003FL10 | 10/18 | ES2 |
| 9 | | HW Design | 0.1 | PG#25 | Change Q6 from SB000002B00 to SB000007H10 | 10/18 | ES2 |
| 10 | | HW Design | 0.1 | PG#10 | Change QC5 from SB523020210 to SB000007600 | 10/18 | ES2 |
| 11 | | NECP | 0.1 | PG#27 | Change R1099 from 10K to 6.8K ohm | 10/18 | ES2 |
| 12 | | HW Design | 0.1 | PG#22 | Change D6 from SCA00001100 to SCA00001L00 | 10/20 | ES2 |
| 13 | | HW Design | 0.1 | PG#26/28 | Change C140/C143/CA1571/CA1572 from SB093475kN0 to SE107475MNO | 10/20 | ES2 |
| 14 | | HW Design | 0.1 | PG#13 | Change CH1/CH2 to 18pF for crystal matching | 10/21 | ES2 |
| 15 | | HW Design | 0.1 | PG#25 | Change C228/C224 to 15pF for crystal matching | 10/21 | ES2 |
| 16 | | HW Design | 0.1 | PG#14 | Change CH22 to 12pF for crystal matching | 10/21 | ES2 |
| 17 | | HW Design | 0.1 | PG#29 | Unmount CS14/CS16/CS17/CS22/CS23/CS24 | 10/24 | ES2 |
| 18 | | HW Design | 0.1 | PG#33 | Change CU28/CU39 to SF000003000 | 10/24 | ES2 |
| 19 | | HW Design | 0.1 | PG#29 | Change CS26 to SF000003000 | 10/24 | ES2 |
| 20 | | HW Design | 0.1 | PG#29 | Add HDD +12VS capacitance CS31/CS32 | 10/24 | ES2 |
| 21 | | HW Design | 0.1 | PG#25 | Change UL18 from SA00003PT10 to SA00004V700(RTL8111F) | 10/24 | ES2 |
| 22 | | HW Design | 0.1 | PG#32 | Update USB3 schematic | 10/24 | ES2 |
| 23 | | HW Design | 0.1 | PG#49 | Update J972 schematic | 10/24 | ES2 |
| 24 | | HW Design | 0.1 | PG#49 | Unmount C1021 | 10/24 | ES2 |
| 25 | | HW Design | 0.1 | PG#30 | Update J99 schematic | 10/24 | ES2 |
| 26 | | HW Design | 0.1 | PG#16 | Unmount CH125 | 10/25 | ES2 |
| 27 | | HW Design | 0.1 | PG#16 | Change CH127/CH128 to 470pF | 10/25 | ES2 |
| 28 | | HW Design | 0.1 | PG#22 | Unmount R1048/R1049 | 10/25 | ES2 |
| 29 | | HW Design | 0.1 | PG#22 | Update PL16/PL17/PL18/PL19/PL20/PL21/PL22/PL23/PL24/PL25/PL26/PL27/PL28/PL29/PL30/PL31/PL32/PL33/PL34/PL35/PL36/PL37/PL38/PL39/PL40/PL41/PL42/PL43/PL44/PL45/PL46/PL47/PL48/PL49/PL50/PL51/PL52/PL53/PL54/PL55/PL56/PL57/PL58/PL59/PL60/PL61/PL62/PL63/PL64/PL65/PL66/PL67/PL68/PL69/PL70/PL71/PL72/PL73/PL74/PL75/PL76/PL77/PL78/PL79/PL80/PL81/PL82/PL83/PL84/PL85/PL86/PL87/PL88/PL89/PL90/PL91/PL92/PL93/PL94/PL95/PL96/PL97/PL98/PL99/PL100/PL101/PL102/PL103/PL104/PL105/PL106/PL107/PL108/PL109/PL110/PL111/PL112/PL113/PL114/PL115/PL116/PL117/PL118/PL119/PL120/PL121/PL122/PL123/PL124/PL125/PL126/PL127/PL128/PL129/PL130/PL131/PL132/PL133/PL134/PL135/PL136/PL137/PL138/PL139/PL140/PL141/PL142/PL143/PL144/PL145/PL146/PL147/PL148/PL149/PL150/PL151/PL152/PL153/PL154/PL155/PL156/PL157/PL158/PL159/PL160/PL161/PL162/PL163/PL164/PL165/PL166/PL167/PL168/PL169/PL170/PL171/PL172/PL173/PL174/PL175/PL176/PL177/PL178/PL179/PL180/PL181/PL182/PL183/PL184/PL185/PL186/PL187/PL188/PL189/PL190/PL191/PL192/PL193/PL194/PL195/PL196/PL197/PL198/PL199/PL200/PL201/PL202/PL203/PL204/PL205/PL206/PL207/PL208/PL209/PL210/PL211/PL212/PL213/PL214/PL215/PL216/PL217/PL218/PL219/PL220/PL221/PL222/PL223/PL224/PL225/PL226/PL227/PL228/PL229/PL230/PL231/PL232/PL233/PL234/PL235/PL236/PL237/PL238/PL239/PL240/PL241/PL242/PL243/PL244/PL245/PL246/PL247/PL248/PL249/PL250/PL251/PL252/PL253/PL254/PL255/PL256/PL257/PL258/PL259/PL260/PL261/PL262/PL263/PL264/PL265/PL266/PL267/PL268/PL269/PL270/PL271/PL272/PL273/PL274/PL275/PL276/PL277/PL278/PL279/PL280/PL281/PL282/PL283/PL284/PL285/PL286/PL287/PL288/PL289/PL290/PL291/PL292/PL293/PL294/PL295/PL296/PL297/PL298/PL299/PL300/PL301/PL302/PL303/PL304/PL305/PL306/PL307/PL308/PL309/PL310/PL311/PL312/PL313/PL314/PL315/PL316/PL317/PL318/PL319/PL320/PL321/PL322/PL323/PL324/PL325/PL326/PL327/PL328/PL329/PL330/PL331/PL332/PL333/PL334/PL335/PL336/PL337/PL338/PL339/PL340/PL341/PL342/PL343/PL344/PL345/PL346/PL347/PL348/PL349/PL350/PL351/PL352/PL353/PL354/PL355/PL356/PL357/PL358/PL359/PL360/PL361/PL362/PL363/PL364/PL365/PL366/PL367/PL368/PL369/PL370/PL371/PL372/PL373/PL374/PL375/PL376/PL377/PL378/PL379/PL380/PL381/PL382/PL383/PL384/PL385/PL386/PL387/PL388/PL389/PL390/PL391/PL392/PL393/PL394/PL395/PL396/PL397/PL398/PL399/PL400/PL401/PL402/PL403/PL404/PL405/PL406/PL407/PL408/PL409/PL410/PL411/PL412/PL413/PL414/PL415/PL416/PL417/PL418/PL419/PL420/PL421/PL422/PL423/PL424/PL425/PL426/PL427/PL428/PL429/PL430/PL431/PL432/PL433/PL434/PL435/PL436/PL437/PL438/PL439/PL440/PL441/PL442/PL443/PL444/PL445/PL446/PL447/PL448/PL449/PL450/PL451/PL452/PL453/PL454/PL455/PL456/PL457/PL458/PL459/PL460/PL461/PL462/PL463/PL464/PL465/PL466/PL467/PL468/PL469/PL470/PL471/PL472/PL473/PL474/PL475/PL476/PL477/PL478/PL479/PL480/PL481/PL482/PL483/PL484/PL485/PL486/PL487/PL488/PL489/PL490/PL491/PL492/PL493/PL494/PL495/PL496/PL497/PL498/PL499/PL500/PL501/PL502/PL503/PL504/PL505/PL506/PL507/PL508/PL509/PL510/PL511/PL512/PL513/PL514/PL515/PL516/PL517/PL518/PL519/PL520/PL521/PL522/PL523/PL524/PL525/PL526/PL527/PL528/PL529/PL530/PL531/PL532/PL533/PL534/PL535/PL536/PL537/PL538/PL539/PL540/PL541/PL542/PL543/PL544/PL545/PL546/PL547/PL548/PL549/PL550/PL551/PL552/PL553/PL554/PL555/PL556/PL557/PL558/PL559/PL560/PL561/PL562/PL563/PL564/PL565/PL566/PL567/PL568/PL569/PL570/PL571/PL572/PL573/PL574/PL575/PL576/PL577/PL578/PL579/PL580/PL581/PL582/PL583/PL584/PL585/PL586/PL587/PL588/PL589/PL590/PL591/PL592/PL593/PL594/PL595/PL596/PL597/PL598/PL599/PL600/PL601/PL602/PL603/PL604/PL605/PL606/PL607/PL608/PL609/PL610/PL611/PL612/PL613/PL614/PL615/PL616/PL617/PL618/PL619/PL620/PL621/PL622/PL623/PL624/PL625/PL626/PL627/PL628/PL629/PL630/PL631/PL632/PL633/PL634/PL635/PL636/PL637/PL638/PL639/PL640/PL641/PL642/PL643/PL644/PL645/PL646/PL647/PL648/PL649/PL650/PL651/PL652/PL653/PL654/PL655/PL656/PL657/PL658/PL659/PL660/PL661/PL662/PL663/PL664/PL665/PL666/PL667/PL668/PL669/PL670/PL671/PL672/PL673/PL674/PL675/PL676/PL677/PL678/PL679/PL680/PL681/PL682/PL683/PL684/PL685/PL686/PL687/PL688/PL689/PL690/PL691/PL692/PL693/PL694/PL695/PL696/PL697/PL698/PL699/PL700/PL701/PL702/PL703/PL704/PL705/PL706/PL707/PL708/PL709/PL710/PL711/PL712/PL713/PL714/PL715/PL716/PL717/PL718/PL719/PL720/PL721/PL722/PL723/PL724/PL725/PL726/PL727/PL728/PL729/PL730/PL731/PL732/PL733/PL734/PL735/PL736/PL737/PL738/PL739/PL740/PL741/PL742/PL743/PL744/PL745/PL746/PL747/PL748/PL749/PL750/PL751/PL752/PL753/PL754/PL755/PL756/PL757/PL758/PL759/PL760/PL761/PL762/PL763/PL764/PL765/PL766/PL767/PL768/PL769/PL770/PL771/PL772/PL773/PL774/PL775/PL776/PL777/PL778/PL779/PL780/PL781/PL782/PL783/PL784/PL785/PL786/PL787/PL788/PL789/PL790/PL791/PL792/PL793/PL794/PL795/PL796/PL797/PL798/PL799/PL800/PL801/PL802/PL803/PL804/PL805/PL806/PL807/PL808/PL809/PL810/PL811/PL812/PL813/PL814/PL815/PL816/PL817/PL818/PL819/PL820/PL821/PL822/PL823/PL824/PL825/PL826/PL827/PL828/PL829/PL830/PL831/PL832/PL833/PL834/PL835/PL836/PL837/PL838/PL839/PL840/PL841/PL842/PL843/PL844/PL845/PL846/PL847/PL848/PL849/PL850/PL851/PL852/PL853/PL854/PL855/PL856/PL857/PL858/PL859/PL860/PL861/PL862/PL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| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
|------|-------------|-------------------|------|-------------|---|-------|-------|
| 1 | | HW Design | 0.2 | PG#10 | Change CC111 from SGA00001Q80 to SGA00005H00 | 12/01 | PP |
| 2 | | HW Design | 0.2 | PG#21/31 | Update schematic for U716 select | 12/07 | PP |
| 3 | | HW Design | 0.2 | PG#23 | Change C789/C790 to 27pF for crystal matching | 12/08 | PP |
| 4 | | HW Design | 0.2 | PG#16/21 | Change U1/U8/UV2/UV3 from SA007080100 to SA741080400 | 12/09 | PP |
| 5 | | HW Design | 0.2 | PG#21/30 | Correct the facatIon from Pc3/PC47hJp25 to cI032/ C1033/Jp24 | 12/09 | PP |
| 6 | | HW Design | 0.2 | PG#10 | Mount CC87/CC88/CC89/CC90 | 12/09 | PP |
| 7 | | HW Design | 0.2 | PG#27 | Update U16(Speaker AMP) mute function schematic | 12/09 | PP |
| 8 | | HW Design | 0.2 | PG#27 | Change UA93/UA94 power from +5VALW to +3VALW | 12/09 | PP |
| 9 | | HW Design | 0.2 | PG#29 | Change F2 from 4A_32V to 5A_32V | 12/15 | PP |
| 10 | | HW Design | 0.2 | PG#29 | Change F8 from 3A_15V to 4A_32V | 12/15 | PP |
| 11 | | HW Design | 0.2 | PG#32 | Change F15 from 5A_32V to 6A_32V | 12/15 | PP |
| 12 | | HW Design | 0.2 | PG#27 | Change UA94 same as UA95 | 12/15 | PP |
| 13 | | HW Design | 0.2 | PG#26/27/28 | Update Audio switch(U725/U726/U732/U735) power | 12/16 | PP |
| 14 | | HW Design | 0.2 | PG#33 | Mount R115/R116/R119/R120 for USB3.0 Redriver | 12/16 | PP |
| 15 | | HW Design | 0.2 | PG#13/27/28 | Change U707/UA95/UA917/UA93/UA94/UA96 from SA007080B90 to SA741080400 | 12/16 | PP |
| 16 | | HW Design | 0.2 | PG#27/28 | Combine U725/U726 switch select pin | 12/16 | PP |
| 17 | | HW Design | 0.2 | PG#28 | Fix the audio ziiiiiii noise | 12/20 | PP |
| 18 | | HW Design | 0.2 | PG#22/23 | To separate the 20" from 21.5" for scalar FW and EDID ROM part | 12/20 | PP |
| 19 | | HW Design | 0.2 | PG#33 | Delete L61 and mount 0ohm | 12/22 | PP |
| 20 | | HW Design | 0.2 | PG#13 | Change CH2 from 18pF to 15pF for crystal matching | 12/22 | PP |
| 21 | | NEC Recommend | 0.2 | PG#28 | Add R1148 R1149 | 12/22 | PP |
| 22 | | HW Design | 0.2 | PG#26 | Net "MIC_SEL" and net "MIC_DET#" pull high change from +3VA_AUDIOSW to +3VALW | 12/26 | PP |
| 23 | | HW Design | 0.2 | PG#8 | Remove RC44 | 12/26 | PP |
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| Item | Fixed Issue | Reason for change | Rev. | PG# | Modify List | Date | Phase |
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